

# e-STUDIO400AC Series

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## Information

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acl, adduser, agg, alsa-lib, alsa-utils, apt, at, attr, audit, base-files, base-passwd, bash, bc, binutils, Buildroot, BusyBox, bzip2, ca-certificates, ccs-tools, chkconfig, cluster-glue, console-setup, coreboot, cpio, cracklib, cron, cryptsetup, cups, CyrusSASL, dbus, dbus-glib, dcfldd, debianutils, dibbler, dmapi, dpkg, e2fsprogs, ElectricFence, elfutils, encryptionSetup, ethtool, fam, fbshot, flashrom, flex, fonts-dejavu, FreeType, ftplib, ftplib++, ftpserver, fuse, gawk, gcc, gdb, gdbm, gettext, glew, glib2, glibc, gmp, gnutls, grub, gstreamer, hdparm, heartbeat, hostname, htop, hwdata, ifupdown, inserv, iproute, IPRTTools, ipsec-tools, iptables, iputils, kbd,	Exhibit 2

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kerberos-config, kexec-tools, keyutils, kmod, krb5, libaio, libbsd, libcap, libcap-ng, libdrm, libev, libevdev, libftdi, libgcrypt, libglu, libgpg-error, libidn, libjpeg, libpng, libprelude, libsamplerate, libselinux, libtasn1, libtirpc, libtool, libtheora, libusb, libxml diff, libxslt, linux-firmware, Linux kernel, lm-sensors, logrotate, lrzsz, lsb, lsof, ltrace, ltng-tools, ltng-ust, LVM2, LZO, lzop, make-dfsg, makedevs, mhash, mingetty, minicom, mpclib, multipath-tools, ncpfs, neon, netatalk, netbase, nettle, net-tools, nfs-utils, Nimbus Sans L font, ntp, openDHCP Server, OpenIPMI, oprofile, Linux-PAM, parted, paxctl, pciutils, perl, policycoreutils, popt, prelink, procps, psmisc, readline, rng-tools, rtk8822BTdriver, rtl8822BU Wi-Fi driver, samhain, sed, sensible-utils, setools, SFML, shadow, slang, smartmontools, strace, sysfsutils, sysklogd, syslog-ng, sysstat, systemd, sysvinit, sysvinit-inittab, tar, tcl, tcpdump, time, trace-cmd, traceroute, tree, uClibc, udev-extraconf, unifont, update-inetd, update-rc.d, usbutils, userspace-rcu, util-linux, V8 JavaScript Engine, valgrind, vim, vsftpd, watchdog, wget, whichman, wireless-tools, x11vnc, x11-xkb-utils, xapian-core, xfsdump, xfsprogs, xkeyboard-config, xz-utils, i2c-tools, dmidecode	
at, autotools-dev, bash, binutils, bison, Boost, Buildroot, coreutils, corosync, cpio, CyrusSASL, diffutils, ed, elfutils, findutils, flex, fontconfig, gawk, gcc, gdb, gettext, glibc, gmp, gnutls, grep, grub, gstreamer, gzip, httpd, ifenslave, inetutils, kbd, less, libcap-ng, libffi, libgcrypt, libidn, libprelude, libtasn1, linux-firmware, Linux-PAM, ltng-tools, ltng-ust, m4, make-dfsg, mesa, ntp, parted, perl, procps, readline, sed, SFML, tar, userspace-rcu, util-linux, wget, xkeyboard-config, xorg-server, xz-utils	Exhibit 3
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libtheora, lsof, make-dfsg, mhash, minicom, multipath-tools, nettle, neon, newt, OpenIPMI, Linux-PAM, prelink, procps, samhain, SFML, systemd, uClibc, valgrind	
acl, alsa-lib, anthy, attr, audit, beecrypt, binutils, Buildroot, cluster-glue, cracklib, cryptsetup, dbus-glib, dmap, elfutils, fam, gawk, gcc, gdb, geoip, gettext, glib2, glibc, gnutls, grub, gstreamer, heartbeat, ivykis, keyutils, kmod, libaio, libcap-ng, libftdi, libgcrypt, libgpg-error, libidn, libnl, libprelude, libqrencode, libsamplerate, libSDL, libsemanage, libsepol, libtasn1, libtheora, libtool, libusb, libutempter, lm-sensors, lttng-tools, lttng-ust, LVM2, mhash, nettle, nfs-utils, OpenIPMI, OpenLDAP, oprofile, prelink, procps, requests, sed, setools, SFML, smartmontools, sysfsutils, syslog-ng, systemd, trace-cmd, traceroute, tree, uClibc, userspace-rcu, ustr, util-linux, valgrind, vim, VxWorksAPI for Linux, wireless-tools, xfsprogs, xz-utils	Exhibit 5
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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

```
<one line to give the program's name and a brief idea of what it does.>
Copyright (C) 19yy <name of author>
```

This program is free software; you can redistribute it and/or modify it under the terms of the GNU General Public License as published by the Free Software Foundation; either version 1, or (at your option) any later version.

This program is distributed in the hope that it will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE. See the GNU General Public License for more details.

You should have received a copy of the GNU General Public License along with this program; if not, write to the Free Software Foundation, Inc., 675 Mass Ave, Cambridge, MA 02139, USA.

Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

```
Gnomovision version 69, Copyright (C) 19xx name of author
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details type `show w'.
This is free software, and you are welcome to redistribute it under certain conditions; type `show c' for details.
```

The hypothetical commands `show w' and `show c' should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w' and `show c'; they could even be mouse-clicks or menu items--whatever suits your program.



You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright interest in the program 'Gnomovision' (a program to direct compilers to make passes at assemblers) written by James Hacker.

<signature of Ty Coon>, 1 April 1989  
Ty Coon, President of Vice

That's all there is to it!

## [Exhibit-2]

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For example, if you distribute copies of such a program, whether gratis or for a fee, you must give the recipients all the rights that you have. You must make sure that they, too, receive or can get the source code. And you must show them these terms so they know their rights.

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Also, for each author's protection and ours, we want to make certain that everyone understands that there is no warranty for this free software. If the software is modified by someone else and passed on, we want its recipients to know that what they have is not the original, so that any problems introduced by others will not reflect on the original authors' reputations.

Finally, any free program is threatened constantly by software patents. We wish to avoid the danger that redistributors of a free program will individually obtain patent licenses, in effect making the program proprietary. To prevent this, we have made it clear that any patent must be licensed for everyone's free use or not licensed at all.

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- b) You must cause any work that you distribute or publish, that in whole or in part contains or is derived from the Program or any part thereof, to be licensed as a whole at no charge to all third parties under the terms of this License.
- c) If the modified program normally reads commands interactively when run, you must cause it, when started running for such interactive use in the most ordinary way, to print or display an announcement including an appropriate copyright notice and a notice that there is no warranty (or else, saying that you provide a warranty) and that users may redistribute the program under these conditions, and telling the user how to view a copy of this License. (Exception: if the Program itself is interactive but does not normally print such an announcement, your work based on the Program is not required to print an announcement.)

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- a) Accompany it with the complete corresponding machine-readable source code, which must be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange; or,
- b) Accompany it with a written offer, valid for at least three years, to give any third party, for a charge no more than your cost of physically performing source distribution, a complete machine-readable copy of the corresponding source code, to be distributed under the terms of Sections 1 and 2 above on a medium customarily used for software interchange; or,
- c) Accompany it with the information you received as to the offer to distribute corresponding source code. (This alternative is allowed only for noncommercial distribution and only if you received the program in object code or executable form with such an offer, in accord with Subsection b above.)

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To do so, attach the following notices to the program. It is safest to attach them to the start of each source file to most effectively convey the exclusion of warranty; and each file should have at least the "copyright" line and a pointer to where the full notice is found.

one line to give the program's name and an idea of what it does.  
Copyright (C) yyyy name of author

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Also add information on how to contact you by electronic and paper mail.

If the program is interactive, make it output a short notice like this when it starts in an interactive mode:

Gnomovision version 69, Copyright (C) year name of author  
Gnomovision comes with ABSOLUTELY NO WARRANTY; for details  
type `show w'. This is free software, and you are welcome  
to redistribute it under certain conditions; type `show c'  
for details.

The hypothetical commands `show w' and `show c' should show the appropriate parts of the General Public License. Of course, the commands you use may be called something other than `show w' and `show c'; they could even be mouse-clicks or menu items--whatever suits your program.

You should also get your employer (if you work as a programmer) or your school, if any, to sign a "copyright disclaimer" for the program, if necessary. Here is a sample; alter the names:

Yoyodyne, Inc., hereby disclaims all copyright  
interest in the program 'Gnomovision'  
(which makes passes at compilers) written  
by James Hacker.

signature of Ty Coon, 1 April 1989  
Ty Coon, President of Vice

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Finally, every program is threatened constantly by software patents. States should not allow patents to restrict development and use of software on general-purpose computers, but in those that do, we wish to avoid the special danger that patents applied to a free program could make it effectively proprietary. To prevent this, the GPL assures that patents cannot be used to render the program non-free.

The precise terms and conditions for copying, distribution and modification follow.

## TERMS AND CONDITIONS

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An interactive user interface displays “Appropriate Legal Notices” to the extent that it includes a convenient and prominently visible feature that (1) displays an appropriate copyright notice, and (2) tells the user that there is no warranty for the work (except to the extent that warranties are provided), that licensees may convey the work under this License, and how to view a copy of this License. If the interface presents a list of user commands or options, such as a menu, a prominent item in the list meets this criterion.

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The “source code” for a work means the preferred form of the work for making modifications to it. “Object code” means any non-source form of a work.

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The Corresponding Source need not include anything that users can regenerate automatically from other parts of the Corresponding Source.

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You may charge any price or no price for each copy that you convey, and you may offer support or warranty protection for a fee.

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You may convey a work based on the Program, or the modifications to produce it from the Program, in the form of source code under the terms of section 4, provided that you also meet all of these conditions:

- \* a) The work must carry prominent notices stating that you modified it, and giving a relevant date.
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A compilation of a covered work with other separate and independent works, which are not by their nature extensions of the covered work, and which are not combined with it such as to form a larger program, in or on a volume of a storage or distribution medium, is called an “aggregate” if the compilation and its resulting copyright are not used to limit the access or legal rights of the compilation's users beyond what the individual works permit. Inclusion of a covered work in an aggregate does not cause this License to apply to the other parts of the aggregate.

## 6. Conveying Non-Source Forms.

You may convey a covered work in object code form under the terms of sections 4 and 5, provided that you also convey the machine-readable Corresponding Source under the terms of this License, in one of these ways:

- \* a) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by the Corresponding Source fixed on a durable physical medium customarily used for software interchange.

- \* b) Convey the object code in, or embodied in, a physical product (including a physical distribution medium), accompanied by a written offer, valid for at least three years and valid for as long as you offer spare parts or customer support for that product model, to give anyone who possesses the object code either (1) a copy of the Corresponding Source for all the software in the product that is covered by this License, on a durable physical medium customarily used for software interchange, for a price no more than your reasonable cost of physically performing this conveying of source, or (2) access to copy the Corresponding Source from a network server at no charge.

- \* c) Convey individual copies of the object code with a copy of the written offer to provide the Corresponding Source. This alternative is allowed only occasionally and noncommercially, and only if you received the object code with such an offer, in accord with subsection 6b.

- \* d) Convey the object code by offering access from a designated place (gratis or for a charge), and offer equivalent access to the Corresponding Source in the same way through the same place at no further charge. You need not require recipients to copy the Corresponding Source along with the object code. If the place to copy the object code is a network server, the Corresponding Source may be on a different server (operated by you or a third party) that supports equivalent copying facilities, provided you maintain clear directions next to the object code saying where to find the Corresponding Source. Regardless of what server hosts the Corresponding Source, you remain obligated to ensure that it is available for as long as needed to satisfy these requirements.

- \* e) Convey the object code using peer-to-peer transmission, provided you inform other peers where the object code and Corresponding Source of the work are being offered to the general public at no charge under subsection 6d.

A separable portion of the object code, whose source code is excluded from the Corresponding Source as a System Library, need not be included in conveying the object code work.

A “User Product” is either (1) a “consumer product”, which means any tangible personal property which is normally used for personal, family, or household purposes, or (2) anything designed or sold for incorporation into a dwelling. In determining whether a product is a consumer product, doubtful cases shall be resolved in favor of coverage. For a particular product received by a particular user, “normally used” refers to a typical or common use of that class of product, regardless of the status of the particular user or of the way in which the particular user actually uses, or expects or is expected to use, the product. A product is a consumer product regardless of whether the product has substantial commercial, industrial or non-consumer uses, unless such uses represent the only significant mode of use of the product.

“Installation Information” for a User Product means any methods, procedures, authorization keys, or other information required to install and execute modified versions of a covered work in that User Product from a modified version of its Corresponding Source. The information must suffice to ensure that the continued functioning of the modified object code is in no case prevented or interfered with solely because modification has been made.

If you convey an object code work under this section in, or with, or specifically for use in, a User Product, and the conveying occurs as part of a transaction in which the right of possession and use of the User Product is transferred to the recipient in perpetuity or for a fixed term (regardless of how the transaction is characterized), the Corresponding Source conveyed under this section must be accompanied by the Installation Information. But this requirement does not apply if neither you nor any third party retains the ability to install modified object code on the User Product (for example, the work has been installed in ROM).

The requirement to provide Installation Information does not include a requirement to continue to provide support service, warranty, or updates for a work that has been modified or installed by the recipient, or for the User Product in which it has been modified or installed. Access to a network may be denied when the modification itself materially and adversely affects



the operation of the network or violates the rules and protocols for communication across the network.

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Version 3, 29 June 2007

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Debian Project, Keita Maehara <maehara@debian.org>, 2003

Debian Project, Kenshi Muto <kmuto@debian.org>, 2004-2012

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Lars Bahner <bahner@debian.org>, 2002-2003.

Axel Bojer <axelb@skolelinux.no>, 2003-2004.

Klaus Ade Johnstad <klaus@skolelinux.no>, 2004.

Bjorn Steensrud <bjornst@powertech.no>, 2004.

Hans Fredrik Nordhaug <hans@nordhaug.priv.no>, 2003, 2005-2010.

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Gustavo Noronha Silva <kov@debian.org>, 2002.

Andre Luis Lopes <andreloup@debian.org>, 2002-2005.

Felipe Augusto van de Wiel (faw) <faw@debian.org>, 2006-2008.

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** THIS IS AN ALTERED VERSION. It was altered by John Gilmore,  
** hoptoad!gnu, on 27 Dec 1986, to add \n as an alternative to |  
** to assist in implementing egrep.  
** THIS IS AN ALTERED VERSION. It was altered by John Gilmore,  
** hoptoad!gnu, on 27 Dec 1986, to add \< and \> for word-matching  
** as in BSD grep and ex.  
** THIS IS AN ALTERED VERSION. It was altered by John Gilmore,  
** hoptoad!gnu, on 28 Dec 1986, to optimize characters quoted with \.  
** THIS IS AN ALTERED VERSION. It was altered by James A. Woods,  
** ames!jaw, on 19 June 1987, to quash a regcomp() redundancy.  
** THIS IS AN ALTERED VERSION. It was altered by Christopher Seiwald  
** seiwald@vix.com, on 28 August 1993, for use in jam. Regmagic.h  
** was moved into regexp.h, and the include of regexp.h now uses ""s  
** to avoid conflicting with the system regexp.h. Const, bless its  
** soul, was removed so it can compile everywhere. The declaration  
** of strchr() was in conflict on AIX, so it was removed (as it is  
** happily defined in string.h).  
** THIS IS AN ALTERED VERSION. It was altered by Christopher Seiwald  
** seiwald@perforce.com, on 20 January 2000, to use function prototypes.
```

Beware that some of this code is subtly aware of the way operator precedence is structured in regular expressions. Serious changes in regular-expression syntax might require a total rethink.

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Then the package has been maintained by Charles Briscoe-Smith <cpbs@debian.org>. I gathered data for the 1999-2001 calendar files from various sources on the Internet, and I'd also like to thank Oliver Elphick, Julian Gilbey, Daniel Martin and Jaldhar H. Vyas for providing much useful data on the various religious calendars. I have edited the files they provided to fit calendar's requirements, so any errors should be attributed to me.

After cpbs@debian.org, Marco d'Itri <md@linux.it> maintained it for almost two years, before Tollef Fog Heen <tfheen@debian.org> took over. The package is now maintained by Graham Wilson <bob@decoy.wox.org>.

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Julian Seward, [jseward@bzip.org](mailto:jseward@bzip.org)  
bzip2/libbzip2 version 1.0.6 of 6 September 2010

## [Exhibit-23]

Format: <http://www.debian.org/doc/packaging-manuals/copyright-format/1.0/>

Source: <http://ftp.debian.org/debian/pool/main/c/ca-certificates/>

Files: debian/\*

examples/\*

Makefile

mozilla/\*

sbin/\*

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/usr/src/gnu/lib/libdialog/dir.c

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Program: dir.c  
Author: Marc van Kempen  
desc: Directory routines, sorting and reading

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100dpi-courR10-IL2.bdf 75dpi-courB12.bdf 75dpi-courB12c.bdf  
75dpi-courB12-IL2.bdf 75dpi-courB14.bdf 75dpi-courB14c.bdf  
75dpi-courB14-IL2.bdf 75dpi-courR12.bdf 75dpi-courR12c.bdf  
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100dpi-lutBS14-IL2.bdf 100dpi-lutBS14.bdf 100dpi-lutBS19-IL2.bdf  
100dpi-lutRS10.bdf 100dpi-lutRS10-IL2.bdf 100dpi-lutRS08.bdf  
100dpi-lutRS08-IL2.bdf 100dpi-lutRS14-IL2.bdf 100dpi-lutRS14.bdf  
100dpi-lutRS19-IL2.bdf 75dpi-lutBS12.bdf 75dpi-lutBS12c.bdf  
75dpi-lutBS12-IL2.bdf 75dpi-lutBS14.bdf 75dpi-lutBS14c.bdf  
75dpi-lutBS14-IL2.bdf 75dpi-lutRS12.bdf 75dpi-lutRS12c.bdf  
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legacy12c.bdf, legacy12d.bdf, legacy14a.bdf, legacy14b.bdf,  
legacy14c.bdf, legacy14d.bdf, legacy14e.bdf, legacy14f.bdf,  
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legacy14k.bdf, legacy14l.bdf, legacy16a.bdf, legacy16b.bdf,  
legacy16c.bdf, legacy16d.bdf, legacy16e.bdf, legacy16f.bdf,  
legacy16g.bdf, legacy16h.bdf, legacy16i.bdf, legacy16j.bdf,  
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#
#          vasprintf          #
#          #
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#          #
#####*/

/*

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Ukrainian Language Locale for Ukraine

Source:

Contact: Volodymyr M. Lisivka

Email: v\_lisivka@users.sourceforge.net

Contact: Max Kutny

Email: mkutny@gmail.com

Language: uk

Territory: UA

Revision: 2.1.12

Date: 2006-05-20

Application: general

Users: general

Charset: UTF-8

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```

/*****
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* 605 E. Springfield, Champaign, IL 61820
* httpd@ncsa.uiuc.edu
*
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*
* md5.c: NCSA HTTPd code which uses the md5c.c RSA Code
*

```

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```
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```

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Written by Adam Twiss (adam@zeus.co.uk). March 1996

Thanks to the following people for their input:

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Originally written by Tony Finch <fanf@demon.net> <dot@dotat.at>.

Implementation ideas were taken from mod\_alias.c. The overall  
concept is derived from the OVERRIDE\_DOC\_ROOT/OVERRIDE\_CGIDIR  
patch to Apache 1.3b3 and a similar feature in Demon's tthtpd,  
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automatic stack-based locking object

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lib/gssapi/mechglue/g\_acquire\_cred.c  
lib/gssapi/mechglue/g\_canon\_name.c  
lib/gssapi/mechglue/g\_compare\_name.c  
lib/gssapi/mechglue/g\_context\_time.c

lib/gssapi/mechglue/g\_delete\_sec\_context.c  
lib/gssapi/mechglue/g\_dsp\_name.c  
lib/gssapi/mechglue/g\_dsp\_status.c  
lib/gssapi/mechglue/g\_dup\_name.c  
lib/gssapi/mechglue/g\_exp\_sec\_context.c  
lib/gssapi/mechglue/g\_export\_name.c  
lib/gssapi/mechglue/g\_glue.c  
lib/gssapi/mechglue/g\_imp\_name.c  
lib/gssapi/mechglue/g\_imp\_sec\_context.c  
lib/gssapi/mechglue/g\_init\_sec\_context.c  
lib/gssapi/mechglue/g\_initialize.c  
lib/gssapi/mechglue/g\_inquire\_context.c  
lib/gssapi/mechglue/g\_inquire\_cred.c  
lib/gssapi/mechglue/g\_inquire\_names.c  
lib/gssapi/mechglue/g\_process\_context.c  
lib/gssapi/mechglue/g\_rel\_buffer.c  
lib/gssapi/mechglue/g\_rel\_cred.c  
lib/gssapi/mechglue/g\_rel\_name.c  
lib/gssapi/mechglue/g\_rel\_oid\_set.c  
lib/gssapi/mechglue/g\_seal.c  
lib/gssapi/mechglue/g\_sign.c  
lib/gssapi/mechglue/g\_store\_cred.c  
lib/gssapi/mechglue/g\_unseal.c  
lib/gssapi/mechglue/g\_userok.c  
lib/gssapi/mechglue/g\_utils.c  
lib/gssapi/mechglue/g\_verify.c  
lib/gssapi/mechglue/gssd\_pname\_to\_uid.c  
lib/gssapi/mechglue/mglueP.h  
lib/gssapi/mechglue/oid\_ops.c  
lib/gssapi/spnego/gssapiP\_spnego.h  
lib/gssapi/spnego/spnego\_mech.c

and the initial implementation of incremental propagation, including the following new or changed files:

include/iprop\_hdr.h  
kadmin/server/ipropd\_svc.c  
lib/kdb/iprop.x  
lib/kdb/kdb\_convert.c  
lib/kdb/kdb\_log.c  
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Here the stream cipher has been modified always to include the time when initializing the state. That makes it impossible to regenerate the same random sequence twice, so this can't be used for encryption, but will generate good random numbers.

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To compute the message digest of a chunk of bytes, declare an  
MD5Context structure, pass it to MD5Init, call MD5Update as  
needed on buffers full of bytes, and then call MD5Final, which  
will fill a supplied 16-byte array with the digest.

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J. Bruce Fields <bfields@umich.edu>  
Olga Kornievskaja <aglo@umich.edu>  
Andy Adamson <andros@umich.edu>

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Christopher Montgomery <monty@xiph.org>  
Greg Maxwell <greg@xiph.org>  
Ralph Giles <giles@xiph.org>  
Cristian Adam <cristian.adam@gmail.com>  
Tim Terriberry <tterribe@xiph.org>

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```
printf("%s",png_get_copyright(NULL));
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Standard RFC1833: Binding Protocols for ONC RPC Version 2  
RFC2078: Generic Security Service Application Program  
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Specification RFC2695: Authentication Mechanisms for ONC RPC

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kernel.

-----

Driver: ambassador -- Madge Ambassador (Collage PCI 155 Server) ATM NIC.

File: atmsar11.fw

Licence: Allegedly GPLv2+, but no source visible. Marked:

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-----

Driver: snd-korg1212 -- Korg 1212 IO audio device

File: korg/k1212.dsp

Licence: Unknown

Found in alsa-firmware package in hex form; no licensing information.

---

Driver: snd-maestro3 -- ESS Allegro Maestro3 audio device

File: ess/maestro3\_assp\_kernel.fw

File: ess/maestro3\_assp\_minisrc.fw

Licence: Unknown

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---

Driver: snd-ymfpci -- Yamaha YMF724/740/744/754 audio devices

File: yamaha/ds1\_ctrl.fw

File: yamaha/ds1\_dsp.fw

File: yamaha/ds1e\_ctrl.fw

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---

Driver: advansys - AdvanSys SCSI

File: advansys/mcode.bin

File: advansys/3550.bin

File: advansys/38C0800.bin

File: advansys/38C1600.bin

Licence: BSD, no source available.

Found in hex form in kernel source.

---

Driver: qla1280 - Qlogic QLA 1240/1x80/1x160 SCSI support

File: qlogic/1040.bin

File: qlogic/1280.bin

File: qlogic/12160.bin

Licence: Redistributable. See LICENCE.qla1280 for details

---

Driver: smctr -- SMC ISA/MCA Token Ring adapter

File: tr\_smctr.bin

Version: 6.3C1



Info: MCT.BIN v6.3C1 03/01/95

Original licence info:

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-----  
Driver: kaweth -- USB KLSI KL5USB101-based Ethernet device

File: kaweth/new\_code.bin  
File: kaweth/new\_code\_fix.bin  
File: kaweth/trigger\_code.bin  
File: kaweth/trigger\_code\_fix.bin

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Found in hex form in the kernel source.

-----  
Driver: dvb-ttusb-budget -- Technotrend/Hauppauge Nova-USB devices

File: ttusb-budget/dspbootcode.bin

Licence: Unknown

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-----  
Driver: keyspan -- USB Keyspan USA-xxx serial device

File: keyspan/mpr.fw  
File: keyspan/usa18x.fw  
File: keyspan/usa19.fw  
File: keyspan/usa19qi.fw  
File: keyspan/usa19qw.fw  
File: keyspan/usa19w.fw  
File: keyspan/usa28.fw  
File: keyspan/usa28xa.fw  
File: keyspan/usa28xb.fw  
File: keyspan/usa28x.fw  
File: keyspan/usa49w.fw  
File: keyspan/usa49wlc.fw

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-----  
Driver: keyspan\_pda -- USB Keyspan PDA single-port serial device

File: keyspan\_pda/keyspan\_pda.fw  
Source: keyspan\_pda/keyspan\_pda.S

File: keyspan\_pda/xircom\_pgs.fw  
Source: keyspan\_pda/xircom\_pgs.S

Source: keyspan\_pda/Makefile

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Compiled from original 8051 source into Intel HEX, used in our binary ihex form.

-----  
Driver: emi26 -- EMI 2|6 USB Audio interface

File: emi26/bitstream.fw  
Version: 1.1.1.131  
Info: DATE=2001dec06

File: emi26/firmware.fw  
Version: 1.0.2.916  
Info: DATE=12.02.2002

File: emi26/loader.fw

Converted from Intel HEX files, used in our binary representation of ihex.

Original licence information:

```
/*
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 *
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 * Emagic EMI 2|6 Audio Interface. Distribution and/or Modification of
 * any driver which includes this firmware, in whole or in part,
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```

\*/

-----  
Driver: emi62 -- EMI 6|2m USB Audio interface

File: emi62/bitstream.fw  
Version: 1.0.0.191  
Info: DATE= 2002oct28

File: emi62/loader.fw  
Version: 1.0.2.002  
Info: DATE=10.01.2002

File: emi62/midi.fw  
Version: 1.04.062  
Info: DATE=16.10.2002

File: emi62/spdif.fw  
Version: 1.04.062  
Info: DATE=16.10.2002

Converted from Intel HEX files, used in our binary representation of ihex.

Original licence information: None

-----  
Driver: ti\_usb\_3410\_5052 -- USB TI 3410/5052 serial device

File: ti\_3410.fw  
Info: firmware 9/10/04 FW3410\_Special\_StartWdogOnStartPort

File: ti\_5052.fw  
Info: firmware 9/18/04

Licence: Allegedly GPLv2+, but no source visible. Marked:  
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Found in hex form in kernel source.

-----  
Driver: ti\_usb\_3410\_5052 -- Multi-Tech USB cell modems

File: mts\_cdma.fw  
File: mts\_gsm.fw  
File: mts\_edge.fw

Licence: "all firmware components are redistributable in binary form"  
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Found in hex form in [ftp://ftp.multitech.com/wireless/wireless\\_linux.zip](ftp://ftp.multitech.com/wireless/wireless_linux.zip)

-----  
Driver: ti\_usb\_3410\_5052 -- Multi-Tech USB fax modems

File: mts\_mt9234mu.fw

File: mts\_mt9234zba.fw

Licence: Unknown

---

Driver: whiteheat -- USB ConnectTech WhiteHEAT serial device

File: whiteheat.fw

Version: 4.06

File: whiteheat\_loader.fw

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Debug loader claims the following behaviour:

Port 1 LED flashes when the vend\_ax program is running

Port 2 LED flashes when any SETUP command arrives

Port 3 LED flashes when any valid VENDOR request occurs

Port 4 LED flashes when the EXTERNAL RAM DOWNLOAD request occurs

Converted from Intel HEX files, used in our binary representation of ihex.

---

Driver: ip2 -- Computone IntelliPort Plus serial device

File: intelliport2.bin

Licence: Unknown

Found in hex form in kernel source.

---

Driver: cpia2 -- cameras based on Vision's CPiA2

File: cpia2/stv0672\_vp4.bin

Licence: Allegedly GPLv2+, but no source visible. Marked:

Copyright (C) 2001 STMicroelectronics, Inc.

Contact: [steve.miller@st.com](mailto:steve.miller@st.com)

Description: This file contains patch data for the CPiA2 (stv0672) VP4.

Found in hex form in kernel source.

---

Driver: dabusb -- Digital Audio Broadcasting (DAB) Receiver for USB and Linux

File: dabusb/firmware.fw

File: dabusb/bitstream.bin

Licence: Distributable

\* Copyright (C) 1999 BayCom GmbH

\*

\* Redistribution and use in source and binary forms, with or without

\* modification, are permitted provided that redistributions of source

\* code retain the above copyright notice and this comment without  
\* modification.

---

Driver: vicam -- USB 3com HomeConnect (aka vicam)

File: vicam/firmware.fw

Licence: Unknown

Found in hex form in kernel source.

---

Driver: io\_edgeport - USB Inside Out Edgeport Serial Driver

File: edgeport/boot.fw

File: edgeport/boot2.fw

File: edgeport/down.fw

File: edgeport/down2.fw

Licence: Allegedly GPLv2+, but no source visible. Marked:

```
*****  
/* Edgeport/4 Binary Image  
/* Generated by HEX2C v1.06  
/* Copyright (C) 1998 Inside Out Networks, All rights reserved.  
*****
```

Found in hex form in kernel source.

---

Driver: io\_ti - USB Inside Out Edgeport Serial Driver  
(TI Devices)

File: edgeport/down3.bin

Licence:

```
*****  
/* Edgeport Binary Image (for TI based products)  
/* Generated by TIBin2C v2.00 (watchport)  
/* Copyright (C) 2001 Inside Out Networks, All rights reserved.  
*****
```

Found in hex form in kernel source.

---

Driver: dsp56k - Atari DSP56k support

File: dsp56k/bootstrap.bin

Source: dsp56k/bootstrap.asm

Source: dsp56k/Makefile

Source: dsp56k/concat-bootstrap.pl

Licence: GPLv2 or later. See GPL-2 and GPL-3 for details.

DSP56001 assembler, buildable with a56 from  
<http://www.zdomain.com/a56.html>

---

Driver: snd-sb16-csp - Sound Blaster 16/AWE CSP support

File: sb16/mulaw\_main.csp  
File: sb16/alaw\_main.csp  
File: sb16/ima\_adpcm\_init.csp  
File: sb16/ima\_adpcm\_playback.csp  
File: sb16/ima\_adpcm\_capture.csp

Licence: Allegedly GPLv2+, but no source visible. Marked:

```
/*  
 * Copyright (c) 1994 Creative Technology Ltd.  
 * Microcode files for SB16 Advanced Signal Processor  
 */
```

Found in hex form in kernel source.

---

Driver: qla2xxx - QLogic QLA2XXX Fibre Channel

File: ql2100\_fw.bin  
Version: 1.19.38 TP  
File: ql2200\_fw.bin  
Version: 2.02.08 TP  
File: ql2300\_fw.bin  
Version: 3.03.28 IPX  
File: ql2322\_fw.bin  
Version: 3.03.28 IPX  
File: ql2400\_fw.bin  
Version: 8.06.00 MID  
File: ql2500\_fw.bin  
Version: 8.06.00 MIDQ

Licence: Redistributable. See LICENCE.qla2xxx for details

Available from <http://ldriver.qlogic.com/firmware/>

---

Driver: orinoco - Agere/Prism/Symbol Orinoco support

File: agere\_sta\_fw.bin  
Version: 9.48 Hermes I  
File: agere\_ap\_fw.bin  
Version: 9.48 Hermes I

Licence: Redistributable. See LICENCE.agere for details

---

Driver: ar9170 - Atheros 802.11n "otus" USB

File: ar9170-1.fw  
File: ar9170-2.fw

Licence: Redistributable. See LICENCE.atheros\_firmware for details

---

Driver: ath9k\_htc - Atheros HTC devices (USB)

File: ar9271.fw  
File: ar7010.fw  
File: ar7010\_1\_1.fw  
File: htc\_9271.fw  
Version: 1.3.1  
File: htc\_7010.fw  
Version: 1.3.1

Licence: Redistributable. See LICENCE.atheros\_firmware for details

File: ath9k\_htc/htc\_7010-1.4.0.fw  
Version: 1.4.0  
File: ath9k\_htc/htc\_9271-1.4.0.fw  
Version: 1.4.0

Licence: Free software. See LICENCE.open-ath9k-htc-firmware for details

---

Driver: cassini - Sun Cassini

File: sun/cassini.bin

Licence: Unknown

Found in hex form in kernel source.

---

Driver: slicoss - Alacritech IS-NIC products

File: slicoss/gbdownload.sys  
File: slicoss/gbrcvcode.sys  
File: slicoss/oasisdbgdownload.sys  
File: slicoss/oasisdownload.sys  
File: slicoss/oasisrcvcode.sys

Licence:

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Permission is hereby granted for the distribution of this firmware image as part of a Linux or other Open Source operating system kernel in text or binary form as required.

This firmware may not be modified.

Found in hex form in kernel source.

---

Driver: sxg - Alacritech IS-NIC products

File: sxg/saharadownloadB.sys

File: sxg/saharadbgdownloadB.sys

Licence:

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Permission is hereby granted for the distribution of this firmware image as part of a Linux or other Open Source operating system kernel in text or binary form as required.

This firmware may not be modified.

Found in hex form in kernel source.

-----  
Driver: cxgb3 - Chelsio Terminator 3 1G/10G Ethernet adapter

File: cxgb3/t3b\_psram-1.1.0.bin

File: cxgb3/t3c\_psram-1.1.0.bin

File: cxgb3/t3fw-7.0.0.bin

File: cxgb3/t3fw-7.1.0.bin

File: cxgb3/t3fw-7.4.0.bin

File: cxgb3/t3fw-7.10.0.bin

File: cxgb3/t3fw-7.12.0.bin

Licence: GPLv2 or OpenIB.org BSD license, no source visible

-----  
Driver: cxgb3 - Chelsio Terminator 3 1G/10G Ethernet adapter

File: cxgb3/ael2005\_opt\_edc.bin

File: cxgb3/ael2005\_twx\_edc.bin

File: cxgb3/ael2020\_twx\_edc.bin

Licence:

\* Copyright (c) 2007-2009 NetLogic Microsystems, Inc.

\*

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\* data in hexadecimal or equivalent format, provided this copyright  
\* notice is accompanying it.

Found in hex form in kernel source.

-----  
Driver: cxgb4 - Chelsio Terminator 4/5/6 1/10/25/40/100G Ethernet adapter

File: cxgb4/t4fw-1.14.4.0.bin

File: cxgb4/t4fw-1.15.37.0.bin

File: cxgb4/t4fw-1.16.33.0.bin



Link: cxgb4/t4fw.bin -> t4fw-1.16.33.0.bin  
File: cxgb4/t5fw-1.14.4.0.bin  
File: cxgb4/t5fw-1.15.37.0.bin  
File: cxgb4/t5fw-1.16.33.0.bin  
Link: cxgb4/t5fw.bin -> t5fw-1.16.33.0.bin  
File: cxgb4/t6fw-1.16.33.0.bin  
Link: cxgb4/t6fw.bin -> t6fw-1.16.33.0.bin  
File: cxgb4/aq1202\_fw.cld  
File: cxgb4/bcm8483.bin

Licence: Redistributable. See LICENCE.chelsio\_firmware for details

---

Driver: e100 -- Intel PRO/100 Ethernet NIC

File: e100/d101m\_ucose.bin  
File: e100/d101s\_ucose.bin  
File: e100/d102e\_ucose.bin

Licence: Redistributable. See LICENCE.e100 for details

---

Driver: acenic -- Alteon AceNIC Gigabit Ethernet card

File: acenic/tg1.bin  
File: acenic/tg2.bin

Licence: Unknown

Found in hex form in kernel source, but source allegedly available at  
<http://alteon.shareable.org/>

---

Driver: tg3 -- Broadcom Tigon3 based gigabit Ethernet cards

File: tigon/tg3.bin  
File: tigon/tg3\_tso.bin  
File: tigon/tg3\_tso5.bin  
File: tigon/tg357766.bin

Licence:

- \* Firmware is:
- \*     Derived from proprietary unpublished source code,
- \*     Copyright (C) 2000-2013 Broadcom Corporation.
- \*
- \*     Permission is hereby granted for the distribution of this firmware
- \*     data in hexadecimal or equivalent format, provided this copyright
- \*     notice is accompanying it.

Found in hex form in kernel source.

---

Driver: starfire - Adaptec Starfire/DuraLAN support

File: adaptec/starfire\_rx.bin  
File: adaptec/starfire\_tx.bin

Licence: Allegedly GPLv2, but no source visible.

Found in hex form in kernel source, with the following notice:

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---

Driver: i2400m-usb - Intel 2400 Wireless WiMAX Connection over USB

File: i2400m-fw-usb-1.4.sbcf

File: i2400m-fw-usb-1.5.sbcf

File: i6050-fw-usb-1.5.sbcf

Licence: Redistributable. See LICENCE.i2400m for details

Also available from <http://linuxwimax.org/Download>

---

Driver: libertas - Marvell Libertas fullmac-type 802.11b/g cards

File: libertas/cf8381.bin

File: libertas/cf8381\_helper.bin

File: libertas/cf8385.bin

File: libertas/cf8385\_helper.bin

File: libertas/gspi8682.bin

File: libertas/gspi8682\_helper.bin

File: libertas/gspi8686\_v9.bin

File: libertas/gspi8686\_v9\_helper.bin

File: libertas/gspi8688.bin

File: libertas/gspi8688\_helper.bin

File: libertas/sd8385.bin

File: libertas/sd8385\_helper.bin

File: libertas/sd8682.bin

File: libertas/sd8682\_helper.bin

File: libertas/sd8686\_v8.bin

File: libertas/sd8686\_v8\_helper.bin

File: libertas/sd8686\_v9.bin

File: libertas/sd8686\_v9\_helper.bin

File: libertas/usb8388\_v5.bin

File: libertas/usb8388\_v9.bin

File: libertas/usb8682.bin

File: mrvl/sd8688.bin

Link: libertas/sd8688.bin -> ../mrvl/sd8688.bin

File: mrvl/sd8688\_helper.bin

Link: libertas/sd8688\_helper.bin -> ../mrvl/sd8688\_helper.bin

Licence: Redistributable. See LICENCE.Marvell for details. Extracted from Linux driver tarballs downloaded from Marvell's "Extranet" with permission.

---

Driver: libertas - Marvell Libertas 802.11b/g cards, OLPC firmware

File: libertas/lbtf\_sdio.bin  
Version: 9.0.7.p4

File: lbtf\_usb.bin  
Version: 5.132.3.p1

File: libertas/usb8388\_olpc.bin  
Version: 5.110.22.p23

Licence: Redistributable. See LICENCE.OLPC for details.

Available from <http://dev.laptop.org/pub/firmware/libertas/>

-----  
Driver: mw18k - Marvell Libertas softmac-type 802.11b/g/n cards

File: mw18k/fmimage\_8687.fw  
File: mw18k/helper\_8687.fw  
File: mw18k/fmimage\_8366.fw  
File: mw18k/fmimage\_8366\_ap-1.fw  
File: mw18k/fmimage\_8366\_ap-2.fw  
File: mw18k/fmimage\_8366\_ap-3.fw  
Version: 5.2.8.16  
File: mw18k/helper\_8366.fw

File: mw18k/fmimage\_8764\_ap-1.fw  
Version: 7.4.0.9

Licence: Redistributable. See LICENCE.Marvell for details. 8687 images downloaded from Marvell's "Extranet" with permission. 8366 images contributed directly by Marvell.

-----  
Driver: mwifiex - Marvell Wi-Fi fullmac-type 802.11n/ac cards

File: mrvl/sd8787\_uapsta.bin  
Version: 14.66.35.p52

File: mrvl/usb8766\_uapsta.bin  
Version: 14.68.22.p16

File: mrvl/sd8797\_uapsta.bin  
Version: 14.66.11.p151

File: mrvl/usb8797\_uapsta.bin  
Version: 14.68.29.p49

File: mrvl/sd8897\_uapsta.bin  
Version: 15.68.7.p87

File: mrvl/usb8897\_uapsta.bin  
Version: 15.68.4.p103

File: mrvl/pcie8897\_uapsta.bin  
Version: 15.68.7.p77

File: mrvl/sd8887\_uapsta.bin  
Version: 15.68.7.p112

File: mrvl/sd8801\_uapsta.bin  
Version: 14.68.36.p60

File: mrvl/usb8801\_uapsta.bin  
Version: 14.68.36.p60

File: mrvl/pcieuart8997\_combo\_v4.bin  
Version: 16.68.1.p70

File: mrvl/pcieusb8997\_combo\_v4.bin  
Version: 16.68.1.p97

File: mrvl/pcie8997\_wlan\_v4.bin  
Version: 16.68.1.p97

Licence: Redistributable. See LICENCE.Marvell for details. Originates from  
<http://git.marvell.com/?p=mwiflex-firmware.git>

-----  
Driver: iwlwifi - Intel Wireless Wifi

File: iwlwifi-3945-2.ucode  
Version: 15.32.2.9

File: iwlwifi-4965-2.ucode  
Version: 228.61.2.24

File: iwlwifi-5000-1.ucode  
Version: 5.4.A.11 (aka 5.4.1.16)

File: iwlwifi-5000-2.ucode  
Version: 8.24.2.12

File: iwlwifi-5000-5.ucode  
Version: 8.83.5.1

File: iwlwifi-5150-2.ucode  
Version: 8.24.2.2

File: iwlwifi-1000-3.ucode  
Version: 128.50.3.1

File: iwlwifi-1000-5.ucode  
Version: 39.31.5.1

File: iwlwifi-6000-4.ucode  
Version: 9.221.4.1

File: iwlwifi-6050-4.ucode  
Version: 9.201.4.1

File: iwlwifi-6050-5.ucode  
Version: 41.28.5.1

File: iwlwifi-6000g2a-5.ucode

Version: 17.168.5.3

File: iwlwifi-6000g2a-6.ucode  
Version: 18.168.6.1

File: iwlwifi-6000g2b-5.ucode  
Version: 17.168.5.1

File: iwlwifi-6000g2b-6.ucode  
Version: 18.168.6.1

File: iwlwifi-135-6.ucode  
Version: 18.168.6.1

File: iwlwifi-100-5.ucode  
Version: 39.31.5.1

File: iwlwifi-105-6.ucode  
Version: 18.168.6.1

File: iwlwifi-2030-6.ucode  
Version: 18.168.6.1

File: iwlwifi-2000-6.ucode  
Version: 18.168.6.1

File: iwlwifi-7260-7.ucode  
Version: 22.1.7.0

File: iwlwifi-7260-8.ucode  
Version: 22.24.8.0

File: iwlwifi-7260-9.ucode  
Version: 25.228.9.0

File: iwlwifi-7260-10.ucode  
Version: 23.15.10.0

File: iwlwifi-7260-12.ucode  
Version: 25.17.12.0

File: iwlwifi-7260-13.ucode  
Version: 25.30.13.0

File: iwlwifi-7260-16.ucode  
Version 16.242414.0

File: iwlwifi-7260-17.ucode  
Version 17.459231.0

File: iwlwifi-3160-7.ucode  
Version: 22.1.7.0

File: iwlwifi-3160-8.ucode  
Version: 22.24.8.0

File: iwlwifi-3160-9.ucode  
Version: 25.228.9.0

File: iwlwifi-3160-10.ucode

Version: 23.15.10.0

File: iwlwifi-3160-12.ucode  
Version: 25.17.12.0

File: iwlwifi-3160-13.ucode  
Version: 25.30.13.0

File: iwlwifi-3160-16.ucode  
Version 16.242414.0

File: iwlwifi-3160-17.ucode  
Version 17.459231.0

File: iwlwifi-7265-8.ucode  
Version: 22.24.8.0

File: iwlwifi-7265-9.ucode  
Version: 25.228.9.0

File: iwlwifi-7265-10.ucode  
Version: 23.15.10.0

File: iwlwifi-7265-12.ucode  
Version: 25.17.12.0

File: iwlwifi-7265-13.ucode  
Version: 25.30.13.0

File: iwlwifi-7265-16.ucode  
Version 16.242414.0

File: iwlwifi-7265-17.ucode  
Version 17.459231.0

File: iwlwifi-7265D-10.ucode  
Version: 23.15.10.0

File: iwlwifi-7265D-12.ucode  
Version: 25.17.12.0

File: iwlwifi-7265D-13.ucode  
Version: 25.30.13.0

File: iwlwifi-7265D-16.ucode  
Version 16.242414.0

File: iwlwifi-7265D-17.ucode  
Version 17.352738.0

File: iwlwifi-7265D-21.ucode  
Version 21.302800.0

File: iwlwifi-7265D-22.ucode  
Version 22.391740.0

File: iwlwifi-7265D-27.ucode  
Version 27.455470.0

File: iwlwifi-3168-21.ucode

Version 21.302800.0

File: iwlwifi-3168-22.ucode  
Version 22.361476.0

File: iwlwifi-3168-27.ucode  
Version 27.455470.0

File: iwlwifi-8000C-13.ucode  
Version: 25.30.13.0

File: iwlwifi-8000C-16.ucode  
Version 16.242414.0

File: iwlwifi-8000C-21.ucode  
Version 21.302800.0

File: iwlwifi-8000C-22.ucode  
Version 22.391740.0

File: iwlwifi-8000C-27.ucode  
Version 27.455470.0

File: iwlwifi-8265-21.ucode  
Version 21.302800.0

File: iwlwifi-8265-22.ucode  
Version 22.391740.0

File: iwlwifi-8265-27.ucode  
Version 27.455470.0

Licence: Redistributable. See LICENCE.iwlwifi\_firmware for details

Also available from <http://wireless.kernel.org/en/users/Drivers/iwlwifi#Firmware>

-----  
Driver: cx231xx - Conexant Cx23100/101/102 USB broadcast A/V decoder

File: v4l-cx231xx-avcore-01.fw

Licence: Redistributable.

Conexant grants permission to use and redistribute these firmware files for use with Conexant devices, but not as a part of the Linux kernel or in any other form which would require these files themselves to be covered by the terms of the GNU General Public License. These firmware files are distributed in the hope that they will be useful, but WITHOUT ANY WARRANTY; without even the implied warranty of MERCHANTABILITY or FITNESS FOR A PARTICULAR PURPOSE.

-----  
Driver: tehuti - Tehuti Networks 10G Ethernet

File: tehuti/bdx.bin

Licence:

Copyright (C) 2007 Tehuti Networks Ltd.

Permission is hereby granted for the distribution of this firmware data in hexadecimal or equivalent format, provided this copyright notice is accompanying it.

Found in hex form in kernel source.

-----  
Driver: typhoon - 3cr990 series Typhoon

File: 3com/typhoon.bin

Licence:

```
/*
 * Copyright 1999-2004 3Com Corporation. All Rights Reserved.
 *
 * Redistribution and use in source and binary forms of the 3c990img.h
 * microcode software are permitted provided that the following conditions
 * are met:
 * 1. Redistribution of source code must retain the above copyright
 * notice, this list of conditions and the following disclaimer.
 * 2. Redistribution in binary form must reproduce the above copyright
 * notice, this list of conditions and the following disclaimer in the
 * documentation and/or other materials provided with the distribution.
 * 3. The name of 3Com may not be used to endorse or promote products
 * derived from this software without specific prior written permission
 *
 * THIS SOFTWARE IS PROVIDED BY 3COM ``AS IS" AND ANY EXPRESS OR
 * IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES
 * OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE ARE DISCLAIMED.
 * IN NO EVENT SHALL THE AUTHOR BE LIABLE FOR ANY DIRECT, INDIRECT,
 * INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES (INCLUDING, BUT
 * NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES; LOSS OF USE,
 * DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY
 * THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
 * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF
 * THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
 *
 * USER ACKNOWLEDGES AND AGREES THAT PURCHASE OR USE OF THE 3c990img.h
 * MICROCODE SOFTWARE WILL NOT CREATE OR GIVE GROUNDS FOR A LICENSE BY
 * IMPLICATION, ESTOPPEL, OR OTHERWISE IN ANY INTELLECTUAL PROPERTY RIGHTS
 * (PATENT, COPYRIGHT, TRADE SECRET, MASK WORK, OR OTHER PROPRIETARY RIGHT)
 * EMBODIED IN ANY OTHER 3COM HARDWARE OR SOFTWARE EITHER SOLELY OR IN
 * COMBINATION WITH THE 3c990img.h MICROCODE SOFTWARE
 */
```

Found in hex form in kernel source.

-----  
Driver: yam - YAM driver for AX.25

File: yam/1200.bin

File: yam/9600.bin

Licence:

\* (C) F6FBB 1998



Found in hex form in kernel source.

---

Driver: 3c359 - 3Com 3C359 Token Link Velocity XL adapter

File: 3com/3C359.bin

Licence:

```
/*
 * The firmware this driver downloads into the tokenring card is a
 * separate program and is not GPL'd source code, even though the Linux
 * side driver and the routine that loads this data into the card are.
 *
 * This firmware is licensed to you strictly for use in conjunction
 * with the use of 3Com 3C359 TokenRing adapters. There is no
 * warranty expressed or implied about its fitness for any purpose.
 */
/* 3c359_microcode.mac: 3Com 3C359 Tokenring microcode.
 *
 * Notes:
 * - Loaded from xl_init upon adapter initialization.
 *
 * Available from 3Com as part of their standard 3C359 driver.
 */
```

Found in hex form in kernel source.

---

Driver: pcnet\_cs - NE2000 compatible PCMCIA adapter

File: cis/LA-PCM.cis  
File: cis/PCMLM28.cis  
File: cis/DP83903.cis  
File: cis/NE2K.cis  
File: cis/tamarack.cis  
File: cis/PE-200.cis  
File: cis/PE520.cis  
Source: cis/

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project  
Copyright (C) 1998, 1999, 2000 David A. Hinds

---

Driver: 3c589\_cs - 3Com PCMCIA adapter

File: cis/3CXEM556.cis  
Source: cis/src/3CXEM556.cis

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project  
Copyright (C) 1998, 1999, 2000 David A. Hinds

---

Driver: 3c574\_cs - 3Com PCMCIA adapter

File: cis/3CCFEM556.cis

Source: cis/src/3CCFEM556.cis

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project  
Copyright (C) 1998, 1999, 2000 David A. Hinds

---

Driver: serial\_cs - Serial PCMCIA adapter

File: cis/MT5634ZLX.cis

File: cis/RS-COM-2P.cis

File: cis/COMpad2.cis

File: cis/COMpad4.cis

Source: cis/src/MT5634ZLX.cis

Source: cis/src/RS-COM-2P.cis

Source: cis/src/COMpad2.cis

Source: cis/src/COMpad4.cis

Licence: Dual GPLv2/MPL

Originally developed by the pcmcia-cs project  
Copyright (C) 1998, 1999, 2000 David A. Hinds

---

Driver: serial\_cs - Serial PCMCIA adapter

File: cis/SW\_555\_SER.cis

File: cis/SW\_7xx\_SER.cis

File: cis/SW\_8xx\_SER.cis

Licence: GPLv3. See GPL-3 for details.

Copyright Sierra Wireless

---

Driver: smc91c92\_cs - SMC 91Cxx PCMCIA

File: ositech/Xilinx7OD.bin

Licence: Allegedly GPL, but no source visible. Marked:  
This file contains the firmware of Seven of Diamonds from OSITECH.  
(Special thanks to Kevin MacPherson of OSITECH)

Found in hex form in kernel source.

---

Driver: cx23418 - Conexant PCI Broadcast A/V with MPEG encoder

File: v4l-cx23418-apu.fw

File: v4l-cx23418-cpu.fw

File: v4l-cx23418-dig.fw

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---

Driver: cx23885 - Conexant PCI Express Broadcast A/V decoder

File: v4l-cx23885-avcore-01.fw

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---

Driver: cx23840 - Conexant sideport Broadcast A/V decoder

File: v4l-cx25840.fw

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---

Driver: qllogicpti - PTI Qlogic, ISP Driver

File: qllogic/isp1000.bin

Licence: Unknown

Found in hex form in kernel source.

---

Driver: myri\_sbus - MyriCOM Gigabit Ethernet

File: myricom/lanai.bin

Licence: Unknown

Found in hex form in kernel source.

-----  
Driver: bnx2x: Broadcom Everest

File: bnx2x-e1-4.8.53.0.fw  
File: bnx2x-e1h-4.8.53.0.fw  
File: bnx2x-e1-5.2.7.0.fw  
File: bnx2x-e1h-5.2.7.0.fw  
File: bnx2x-e1-5.2.13.0.fw  
File: bnx2x-e1h-5.2.13.0.fw  
File: bnx2x/bnx2x-e1-6.0.34.0.fw  
File: bnx2x/bnx2x-e1h-6.0.34.0.fw  
File: bnx2x/bnx2x-e2-6.0.34.0.fw  
File: bnx2x/bnx2x-e1-6.2.5.0.fw  
File: bnx2x/bnx2x-e1h-6.2.5.0.fw  
File: bnx2x/bnx2x-e2-6.2.5.0.fw  
File: bnx2x/bnx2x-e1-6.2.9.0.fw  
File: bnx2x/bnx2x-e1h-6.2.9.0.fw  
File: bnx2x/bnx2x-e2-6.2.9.0.fw  
File: bnx2x/bnx2x-e2-7.0.20.0.fw  
File: bnx2x/bnx2x-e1-7.0.20.0.fw  
File: bnx2x/bnx2x-e1h-7.0.20.0.fw  
File: bnx2x/bnx2x-e2-7.0.23.0.fw  
File: bnx2x/bnx2x-e1-7.0.23.0.fw  
File: bnx2x/bnx2x-e1h-7.0.23.0.fw  
File: bnx2x/bnx2x-e2-7.0.29.0.fw  
File: bnx2x/bnx2x-e1-7.0.29.0.fw  
File: bnx2x/bnx2x-e1h-7.0.29.0.fw  
File: bnx2x/bnx2x-e2-7.2.16.0.fw  
File: bnx2x/bnx2x-e1-7.2.16.0.fw  
File: bnx2x/bnx2x-e1h-7.2.16.0.fw  
File: bnx2x/bnx2x-e2-7.2.51.0.fw  
File: bnx2x/bnx2x-e1-7.2.51.0.fw  
File: bnx2x/bnx2x-e1h-7.2.51.0.fw  
File: bnx2x/bnx2x-e1-7.8.2.0.fw  
File: bnx2x/bnx2x-e1h-7.8.2.0.fw  
File: bnx2x/bnx2x-e2-7.8.2.0.fw  
File: bnx2x/bnx2x-e1-7.8.17.0.fw  
File: bnx2x/bnx2x-e1h-7.8.17.0.fw  
File: bnx2x/bnx2x-e2-7.8.17.0.fw  
File: bnx2x/bnx2x-e1-7.8.19.0.fw  
File: bnx2x/bnx2x-e1h-7.8.19.0.fw  
File: bnx2x/bnx2x-e2-7.8.19.0.fw  
File: bnx2x/bnx2x-e1-7.10.51.0.fw  
File: bnx2x/bnx2x-e1h-7.10.51.0.fw  
File: bnx2x/bnx2x-e2-7.10.51.0.fw  
File: bnx2x/bnx2x-e1-7.12.30.0.fw  
File: bnx2x/bnx2x-e1h-7.12.30.0.fw  
File: bnx2x/bnx2x-e2-7.12.30.0.fw  
File: bnx2x/bnx2x-e1-7.13.1.0.fw  
File: bnx2x/bnx2x-e1h-7.13.1.0.fw  
File: bnx2x/bnx2x-e2-7.13.1.0.fw

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Found in hex form in kernel source.

-----  
Driver: bnx2 - Broadcom NetXtremeII

File: bnx2/bnx2-mips-06-4.6.16.fw  
File: bnx2/bnx2-mips-06-5.0.0.j3.fw  
File: bnx2/bnx2-mips-06-5.0.0.j6.fw  
File: bnx2/bnx2-mips-06-6.0.15.fw  
File: bnx2/bnx2-mips-06-6.2.1.fw  
File: bnx2/bnx2-mips-06-6.2.3.fw  
File: bnx2/bnx2-mips-09-4.6.17.fw  
File: bnx2/bnx2-mips-09-5.0.0.j3.fw  
File: bnx2/bnx2-mips-09-5.0.0.j9.fw  
File: bnx2/bnx2-mips-09-5.0.0.j15.fw  
File: bnx2/bnx2-mips-09-6.0.17.fw  
File: bnx2/bnx2-mips-09-6.2.1.fw  
File: bnx2/bnx2-mips-09-6.2.1a.fw  
File: bnx2/bnx2-mips-09-6.2.1b.fw  
File: bnx2/bnx2-rv2p-06-4.6.16.fw  
File: bnx2/bnx2-rv2p-06-5.0.0.j3.fw  
File: bnx2/bnx2-rv2p-06-6.0.15.fw  
File: bnx2/bnx2-rv2p-09-4.6.15.fw  
File: bnx2/bnx2-rv2p-09-5.0.0.j3.fw  
File: bnx2/bnx2-rv2p-09-5.0.0.j10.fw  
File: bnx2/bnx2-rv2p-09-6.0.17.fw  
File: bnx2/bnx2-rv2p-09ax-5.0.0.j3.fw  
File: bnx2/bnx2-rv2p-09ax-5.0.0.j10.fw  
File: bnx2/bnx2-rv2p-09ax-6.0.17.fw

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-----  
Driver: netxen\_nic - NetXen Multi port (1/10) Gigabit Ethernet NIC

File: phanfw.bin  
Version: 4.0.590

Licence: Redistributable. See LICENCE.phanfw for details.

Available from [http://ldriver.qlogic.com/firmware/netxen\\_nic/new/](http://ldriver.qlogic.com/firmware/netxen_nic/new/)

-----  
Driver: dvb-ttpci -- AV7110 cards

File: av7110/bootcode.bin  
Source: av7110/Boot.S  
Source: av7110/Makefile

Licence: GPLv2 or later. See GPLv2 and GPLv3 for details.

ARM assembly source code from <https://linuxtv.org/downloads/firmware/Boot.S>

-----  
Driver: snd-wavefront - ISA WaveFront sound card

File: yamaha/yss225\_registers.bin

Licence: Allegedly GPLv2+, but no source visible.

Found in hex form in kernel source, with the following comment:  
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-----  
Driver: rt61pci - Ralink RT2561, RT2561S, RT2661 wireless MACs

File: rt2561.bin  
File: rt2561s.bin  
File: rt2661.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Downloaded from <http://www.ralinktech.com/ralink/Home/Support/Linux.html>

-----  
Driver: rt73usb - Ralink RT2571W, RT2671 wireless MACs

File: rt73.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Downloaded from <http://www.ralinktech.com/ralink/Home/Support/Linux.html>

-----  
Driver: mt7601u - MediaTek MT7601U Wireless MACs

File: mt7601u.bin  
Version: 34

Licence: Redistributable. See LICENCE.ralink\_a\_mEDIATEK\_company\_firmware for details

Downloaded from <http://www.mediatek.com/en/downloads/>

-----  
Driver: rt2800pci - Ralink RT2860, RT2890, RT3090, RT3290, RT5390 wireless MACs

File: rt2860.bin  
Version: 40

File: rt3290.bin  
Version: 37

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

Binary file supplied originally by Shiang Tu <shiang\_tu@ralinktech.com>, latest  
from <http://www.mediatek.com/en/downloads1/downloads/>

-----  
Driver: rt2860sta - Ralink RT3090 wireless MACs

Link: rt3090.bin -> rt2860.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

-----  
Driver: rt2800usb - Ralink RT2870, RT3070, RT3071, RT3072, RT5370 wireless MACs

File: rt2870.bin  
Version: 36

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Binary file supplied originally by Shiang Tu <shiang\_tu@ralinktech.com>, latest  
from <http://www.mediatek.com/en/downloads1/downloads/>

-----  
Driver: rt2870sta - Ralink RT2870, RT3070, RT3071 wireless MACs

Link: rt3070.bin -> rt2870.bin  
File: rt3071.bin

Licence: Redistributable. See LICENCE.ralink-firmware.txt for details

rt3071.bin is a copy of bytes 4096-8191 of rt2870.bin for compatibility.

-----  
Driver: usbdux/usbduxfast/usbduxsigma - usbdux data acquisition cards

File: usbdux\_firmware.bin  
File: usbduxfast\_firmware.bin  
File: usbduxsigma\_firmware.bin  
Source: usbdux/

Licence: GPLv2. See GPLv2 for details.

Provided from the author, Bernd Porr <BerndPorr@f2s.com>

-----  
Driver: xc4000 - Xceive 4000 Tuner driver

File: dvb-fe-xc4000-1.4.1.fw

Version: 1.4.1

Licence: Redistributable. See LICENCE.xc4000 for details

---

Driver: xc5000 - Xceive 5000 Tuner driver

File: dvb-fe-xc5000-1.6.114.fw

Version: 1.6.114

File: dvb-fe-xc5000c-4.1.30.7.fw

Version: 4.1.30.7

Licence: Redistributable. See LICENCE.xc5000 and LICENCE.xc5000c for details

---

Driver: dib0700 - DiBcom dib0700 USB DVB bridge driver

File: dvb-usb-dib0700-1.20.fw

Version: 1.20

Licence: Redistributable. See LICENSE.dib0700 for details

---

Driver: ath3k - DFU Driver for Atheros bluetooth chipset AR3011

File: ath3k-1.fw

Version: 1.0

Fix EEPROM radio table issue and change PID to 3005

Licence: Redistributable. See LICENCE.atheros\_firmware for details

---

Driver: mga - Matrox G200/G400/G550

File: matrox/g200\_warp.fw

File: matrox/g400\_warp.fw

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Found in hex form in kernel source.

-----  
Driver: r128 - ATI Rage 128

File: r128/r128\_cce.bin

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-----  
Driver: radeon - ATI Radeon

File: radeon/R100\_cp.bin  
File: radeon/R200\_cp.bin  
File: radeon/R300\_cp.bin  
File: radeon/R420\_cp.bin  
File: radeon/RS600\_cp.bin  
File: radeon/RS690\_cp.bin  
File: radeon/R520\_cp.bin  
File: radeon/R600\_pfp.bin  
File: radeon/R600\_me.bin  
File: radeon/RV610\_pfp.bin  
File: radeon/RV610\_me.bin  
File: radeon/RV630\_pfp.bin  
File: radeon/RV630\_me.bin  
File: radeon/RV620\_pfp.bin  
File: radeon/RV620\_me.bin  
File: radeon/RV635\_pfp.bin  
File: radeon/RV635\_me.bin  
File: radeon/RV670\_pfp.bin

File: radeon/RV670\_me.bin  
File: radeon/RS780\_pfp.bin  
File: radeon/RS780\_me.bin  
File: radeon/RV770\_pfp.bin  
File: radeon/RV770\_me.bin  
File: radeon/RV730\_pfp.bin  
File: radeon/RV730\_me.bin  
File: radeon/RV710\_pfp.bin  
File: radeon/RV710\_me.bin

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Found in hex form in kernel source.

-----  
Driver: radeon - ATI Radeon

File: radeon/R600\_rlc.bin  
File: radeon/R600\_uvd.bin  
File: radeon/RS780\_uvd.bin  
File: radeon/R700\_rlc.bin  
File: radeon/RV710\_uvd.bin  
File: radeon/RV710\_smc.bin  
File: radeon/RV730\_smc.bin  
File: radeon/RV740\_smc.bin  
File: radeon/RV770\_smc.bin  
File: radeon/RV770\_uvd.bin  
File: radeon/CEDAR\_me.bin  
File: radeon/CEDAR\_pfp.bin  
File: radeon/CEDAR\_rlc.bin  
File: radeon/CEDAR\_smc.bin  
File: radeon/CYPRESS\_me.bin  
File: radeon/CYPRESS\_pfp.bin  
File: radeon/CYPRESS\_rlc.bin  
File: radeon/CYPRESS\_uvd.bin  
File: radeon/CYPRESS\_smc.bin  
File: radeon/JUNIPER\_me.bin

File: radeon/JUNIPER\_pfp.bin  
File: radeon/JUNIPER\_rlc.bin  
File: radeon/JUNIPER\_smc.bin  
File: radeon/REDWOOD\_me.bin  
File: radeon/REDWOOD\_pfp.bin  
File: radeon/REDWOOD\_rlc.bin  
File: radeon/REDWOOD\_smc.bin  
File: radeon/PALM\_me.bin  
File: radeon/PALM\_pfp.bin  
File: radeon/SUMO\_rlc.bin  
File: radeon/SUMO\_uvd.bin  
File: radeon/BARTS\_mc.bin  
File: radeon/BARTS\_me.bin  
File: radeon/BARTS\_pfp.bin  
File: radeon/BARTS\_smc.bin  
File: radeon/BTC\_rlc.bin  
File: radeon/CAICOS\_mc.bin  
File: radeon/CAICOS\_me.bin  
File: radeon/CAICOS\_pfp.bin  
File: radeon/CAICOS\_smc.bin  
File: radeon/TURKS\_mc.bin  
File: radeon/TURKS\_me.bin  
File: radeon/TURKS\_pfp.bin  
File: radeon/TURKS\_smc.bin  
File: radeon/CAYMAN\_mc.bin  
File: radeon/CAYMAN\_me.bin  
File: radeon/CAYMAN\_pfp.bin  
File: radeon/CAYMAN\_rlc.bin  
File: radeon/CAYMAN\_smc.bin  
File: radeon/SUMO\_pfp.bin  
File: radeon/SUMO\_me.bin  
File: radeon/SUMO2\_pfp.bin  
File: radeon/SUMO2\_me.bin  
File: radeon/ARUBA\_me.bin  
File: radeon/ARUBA\_pfp.bin  
File: radeon/ARUBA\_rlc.bin  
File: radeon/PITCAIRN\_ce.bin  
File: radeon/PITCAIRN\_mc.bin  
File: radeon/PITCAIRN\_mc2.bin  
File: radeon/PITCAIRN\_me.bin  
File: radeon/PITCAIRN\_pfp.bin  
File: radeon/PITCAIRN\_rlc.bin  
File: radeon/PITCAIRN\_smc.bin  
File: radeon/TAHITI\_ce.bin  
File: radeon/TAHITI\_mc.bin  
File: radeon/TAHITI\_mc2.bin  
File: radeon/TAHITI\_me.bin  
File: radeon/TAHITI\_pfp.bin  
File: radeon/TAHITI\_rlc.bin  
File: radeon/TAHITI\_uvd.bin  
File: radeon/TAHITI\_smc.bin  
File: radeon/TAHITI\_vce.bin  
File: radeon/VERDE\_ce.bin  
File: radeon/VERDE\_mc.bin  
File: radeon/VERDE\_mc2.bin  
File: radeon/VERDE\_me.bin  
File: radeon/VERDE\_pfp.bin  
File: radeon/VERDE\_rlc.bin  
File: radeon/VERDE\_smc.bin  
File: radeon/OLAND\_ce.bin

File: radeon/OLAND\_mc.bin  
File: radeon/OLAND\_mc2.bin  
File: radeon/OLAND\_me.bin  
File: radeon/OLAND\_pfp.bin  
File: radeon/OLAND\_rlc.bin  
File: radeon/OLAND\_smc.bin  
File: radeon/HAINAN\_ce.bin  
File: radeon/HAINAN\_mc.bin  
File: radeon/HAINAN\_mc2.bin  
File: radeon/HAINAN\_me.bin  
File: radeon/HAINAN\_pfp.bin  
File: radeon/HAINAN\_rlc.bin  
File: radeon/HAINAN\_smc.bin  
File: radeon/BONAIRE\_ce.bin  
File: radeon/BONAIRE\_mc.bin  
File: radeon/BONAIRE\_mc2.bin  
File: radeon/BONAIRE\_me.bin  
File: radeon/BONAIRE\_mec.bin  
File: radeon/BONAIRE\_pfp.bin  
File: radeon/BONAIRE\_rlc.bin  
File: radeon/BONAIRE\_sdma.bin  
File: radeon/BONAIRE\_uvd.bin  
File: radeon/BONAIRE\_smc.bin  
File: radeon/BONAIRE\_vce.bin  
File: radeon/KABINI\_ce.bin  
File: radeon/KABINI\_me.bin  
File: radeon/KABINI\_mec.bin  
File: radeon/KABINI\_pfp.bin  
File: radeon/KABINI\_rlc.bin  
File: radeon/KABINI\_sdma.bin  
File: radeon/KAVERI\_ce.bin  
File: radeon/KAVERI\_me.bin  
File: radeon/KAVERI\_mec.bin  
File: radeon/KAVERI\_pfp.bin  
File: radeon/KAVERI\_rlc.bin  
File: radeon/KAVERI\_sdma.bin  
File: radeon/HAWAII\_ce.bin  
File: radeon/HAWAII\_mc.bin  
File: radeon/HAWAII\_mc2.bin  
File: radeon/HAWAII\_me.bin  
File: radeon/HAWAII\_mec.bin  
File: radeon/HAWAII\_pfp.bin  
File: radeon/HAWAII\_rlc.bin  
File: radeon/HAWAII\_sdma.bin  
File: radeon/HAWAII\_smc.bin  
File: radeon/MULLINS\_ce.bin  
File: radeon/MULLINS\_me.bin  
File: radeon/MULLINS\_mec.bin  
File: radeon/MULLINS\_pfp.bin  
File: radeon/MULLINS\_rlc.bin  
File: radeon/MULLINS\_sdma.bin  
File: radeon/pitcairn\_ce.bin  
File: radeon/pitcairn\_k\_smc.bin  
File: radeon/pitcairn\_mc.bin  
File: radeon/pitcairn\_me.bin  
File: radeon/pitcairn\_pfp.bin  
File: radeon/pitcairn\_rlc.bin  
File: radeon/pitcairn\_smc.bin  
File: radeon/tahiti\_ce.bin  
File: radeon/tahiti\_k\_smc.bin

File: radeon/tahiti\_mc.bin  
File: radeon/tahiti\_me.bin  
File: radeon/tahiti\_pfp.bin  
File: radeon/tahiti\_rlc.bin  
File: radeon/tahiti\_smc.bin  
File: radeon/verde\_ce.bin  
File: radeon/verde\_k\_smc.bin  
File: radeon/verde\_mc.bin  
File: radeon/verde\_me.bin  
File: radeon/verde\_pfp.bin  
File: radeon/verde\_rlc.bin  
File: radeon/verde\_smc.bin  
File: radeon/oland\_ce.bin  
File: radeon/oland\_k\_smc.bin  
File: radeon/oland\_mc.bin  
File: radeon/oland\_me.bin  
File: radeon/oland\_pfp.bin  
File: radeon/oland\_rlc.bin  
File: radeon/oland\_smc.bin  
File: radeon/hainan\_ce.bin  
File: radeon/hainan\_k\_smc.bin  
File: radeon/hainan\_mc.bin  
File: radeon/hainan\_me.bin  
File: radeon/hainan\_pfp.bin  
File: radeon/hainan\_rlc.bin  
File: radeon/hainan\_smc.bin  
File: radeon/bonaire\_ce.bin  
File: radeon/bonaire\_k\_smc.bin  
File: radeon/bonaire\_mc.bin  
File: radeon/bonaire\_me.bin  
File: radeon/bonaire\_mec.bin  
File: radeon/bonaire\_pfp.bin  
File: radeon/bonaire\_rlc.bin  
File: radeon/bonaire\_sdma.bin  
File: radeon/bonaire\_sdma1.bin  
File: radeon/bonaire\_smc.bin  
File: radeon/bonaire\_uvd.bin  
File: radeon/bonaire\_vce.bin  
File: radeon/kabini\_ce.bin  
File: radeon/kabini\_me.bin  
File: radeon/kabini\_mec.bin  
File: radeon/kabini\_pfp.bin  
File: radeon/kabini\_rlc.bin  
File: radeon/kabini\_sdma.bin  
File: radeon/kabini\_sdma1.bin  
File: radeon/kabini\_uvd.bin  
File: radeon/kabini\_vce.bin  
File: radeon/kaveri\_ce.bin  
File: radeon/kaveri\_me.bin  
File: radeon/kaveri\_mec.bin  
File: radeon/kaveri\_mec2.bin  
File: radeon/kaveri\_pfp.bin  
File: radeon/kaveri\_rlc.bin  
File: radeon/kaveri\_sdma.bin  
File: radeon/kaveri\_sdma1.bin  
File: radeon/kaveri\_uvd.bin  
File: radeon/kaveri\_vce.bin  
File: radeon/hawaii\_ce.bin  
File: radeon/hawaii\_k\_smc.bin  
File: radeon/hawaii\_mc.bin

File: radeon/hawaii\_me.bin  
File: radeon/hawaii\_mec.bin  
File: radeon/hawaii\_pfp.bin  
File: radeon/hawaii\_rlc.bin  
File: radeon/hawaii\_sdma.bin  
File: radeon/hawaii\_sdma1.bin  
File: radeon/hawaii\_smc.bin  
File: radeon/hawaii\_uvd.bin  
File: radeon/hawaii\_vce.bin  
File: radeon/mullins\_ce.bin  
File: radeon/mullins\_me.bin  
File: radeon/mullins\_mec.bin  
File: radeon/mullins\_pfp.bin  
File: radeon/mullins\_rlc.bin  
File: radeon/mullins\_sdma.bin  
File: radeon/mullins\_sdma1.bin  
File: radeon/mullins\_uvd.bin  
File: radeon/mullins\_vce.bin

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-----  
Driver: amdgpu - AMD Radeon

File: amdgpu/topaz\_ce.bin  
File: amdgpu/topaz\_k\_smc.bin  
File: amdgpu/topaz\_mc.bin  
File: amdgpu/topaz\_me.bin  
File: amdgpu/topaz\_mec2.bin  
File: amdgpu/topaz\_mec.bin  
File: amdgpu/topaz\_pfp.bin  
File: amdgpu/topaz\_rlc.bin  
File: amdgpu/topaz\_sdma1.bin  
File: amdgpu/topaz\_sdma.bin  
File: amdgpu/topaz\_smc.bin  
File: amdgpu/tonga\_ce.bin  
File: amdgpu/tonga\_k\_smc.bin  
File: amdgpu/tonga\_mc.bin  
File: amdgpu/tonga\_me.bin  
File: amdgpu/tonga\_mec2.bin  
File: amdgpu/tonga\_mec.bin  
File: amdgpu/tonga\_pfp.bin  
File: amdgpu/tonga\_rlc.bin  
File: amdgpu/tonga\_sdma1.bin  
File: amdgpu/tonga\_sdma.bin  
File: amdgpu/tonga\_smc.bin  
File: amdgpu/tonga\_uvd.bin  
File: amdgpu/tonga\_vce.bin  
File: amdgpu/carrizo\_ce.bin  
File: amdgpu/carrizo\_me.bin  
File: amdgpu/carrizo\_mec2.bin  
File: amdgpu/carrizo\_mec.bin  
File: amdgpu/carrizo\_pfp.bin  
File: amdgpu/carrizo\_rlc.bin  
File: amdgpu/carrizo\_sdma1.bin  
File: amdgpu/carrizo\_sdma.bin  
File: amdgpu/carrizo\_uvd.bin  
File: amdgpu/carrizo\_vce.bin  
File: amdgpu/fiji\_ce.bin

File: amdgpu/fiji\_mc.bin  
File: amdgpu/fiji\_me.bin  
File: amdgpu/fiji\_mec2.bin  
File: amdgpu/fiji\_mec.bin  
File: amdgpu/fiji\_pfp.bin  
File: amdgpu/fiji\_rlc.bin  
File: amdgpu/fiji\_sdma1.bin  
File: amdgpu/fiji\_sdma.bin  
File: amdgpu/fiji\_smc.bin  
File: amdgpu/fiji\_uvd.bin  
File: amdgpu/fiji\_vce.bin  
File: amdgpu/stoney\_ce.bin  
File: amdgpu/stoney\_me.bin  
File: amdgpu/stoney\_mec.bin  
File: amdgpu/stoney\_pfp.bin  
File: amdgpu/stoney\_rlc.bin  
File: amdgpu/stoney\_sdma.bin  
File: amdgpu/stoney\_uvd.bin  
File: amdgpu/stoney\_vce.bin  
File: amdgpu/polaris10\_ce.bin  
File: amdgpu/polaris10\_mc.bin  
File: amdgpu/polaris10\_me.bin  
File: amdgpu/polaris10\_mec2.bin  
File: amdgpu/polaris10\_mec.bin  
File: amdgpu/polaris10\_pfp.bin  
File: amdgpu/polaris10\_rlc.bin  
File: amdgpu/polaris10\_sdma1.bin  
File: amdgpu/polaris10\_sdma.bin  
File: amdgpu/polaris10\_smc.bin  
File: amdgpu/polaris10\_k\_smc.bin  
File: amdgpu/polaris10\_smc\_sk.bin  
File: amdgpu/polaris10\_uvd.bin  
File: amdgpu/polaris10\_vce.bin  
File: amdgpu/polaris11\_ce.bin  
File: amdgpu/polaris11\_mc.bin  
File: amdgpu/polaris11\_me.bin  
File: amdgpu/polaris11\_mec2.bin  
File: amdgpu/polaris11\_mec.bin  
File: amdgpu/polaris11\_pfp.bin  
File: amdgpu/polaris11\_rlc.bin  
File: amdgpu/polaris11\_sdma1.bin  
File: amdgpu/polaris11\_sdma.bin  
File: amdgpu/polaris11\_smc.bin  
File: amdgpu/polaris11\_k\_smc.bin  
File: amdgpu/polaris11\_smc\_sk.bin  
File: amdgpu/polaris11\_uvd.bin  
File: amdgpu/polaris11\_vce.bin  
File: amdgpu/polaris12\_ce.bin  
File: amdgpu/polaris12\_mc.bin  
File: amdgpu/polaris12\_me.bin  
File: amdgpu/polaris12\_mec.bin  
File: amdgpu/polaris12\_mec2.bin  
File: amdgpu/polaris12\_pfp.bin  
File: amdgpu/polaris12\_rlc.bin  
File: amdgpu/polaris12\_sdma.bin  
File: amdgpu/polaris12\_sdma1.bin  
File: amdgpu/polaris12\_smc.bin  
File: amdgpu/polaris12\_uvd.bin  
File: amdgpu/polaris12\_vce.bin

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---

Driver: s2255drv

File: f2255usb.bin

Licence: Redistributable.

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---

Driver: rtl8192e - Realtek 8192 PCI wireless driver

File: RTL8192E/boot.img

File: RTL8192E/data.img

File: RTL8192E/main.img

Licence: Redistributable, provided by Realtek in their driver source download.

---

Driver: lgs8gxx - Legend Silicon GB20600 demodulator driver

File: lgs8g75.fw

Licence: Unknown

---

Driver: ib\_qib - QLogic Infiniband

File: qllogic/sd7220.fw

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Found in hex form in kernel source.

-----  
Driver: qed - QLogic 4xxxx Ethernet Driver Core Module.

File: qed/qed\_init\_values\_zipped-8.4.2.0.bin  
File: qed/qed\_init\_values\_zipped-8.7.3.0.bin  
File: qed/qed\_init\_values\_zipped-8.10.5.0.bin  
File: qed/qed\_init\_values\_zipped-8.10.10.0.bin  
File: qed/qed\_init\_values\_zipped-8.15.3.0.bin

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-----  
Driver: ueagle-atm - Driver for USB ADSL Modems based on Eagle IV Chipset

File: ueagle-atm/CMV4p.bin.v2  
File: ueagle-atm/DSP4p.bin  
File: ueagle-atm/eagleIV.fw  
Version: 1.0

Licence: Redistributable. See LICENCE.ueagle-atm4-firmware for details

-----  
Driver: ueagle-atm - Driver for USB ADSL Modems based on Eagle I,II,III

File: ueagle-atm/930-fpga.bin  
File: ueagle-atm/CMVeIWO.bin  
File: ueagle-atm/CMVepFR10.bin  
File: ueagle-atm/DSP9p.bin  
File: ueagle-atm/eagleIII.fw  
File: ueagle-atm/adi930.fw  
File: ueagle-atm/CMVep.bin  
File: ueagle-atm/CMVepFR.bin  
File: ueagle-atm/DSPei.bin

File: ueagle-atm/CMV9i.bin  
File: ueagle-atm/CMVepES03.bin  
File: ueagle-atm/CMVepIT.bin  
File: ueagle-atm/DSPep.bin  
File: ueagle-atm/CMV9p.bin  
File: ueagle-atm/CMVepES.bin  
File: ueagle-atm/CMVepWO.bin  
File: ueagle-atm/eagleI.fw  
File: ueagle-atm/CMVei.bin  
File: ueagle-atm/CMVepFR04.bin  
File: ueagle-atm/DSP9i.bin  
File: ueagle-atm/eagleII.fw  
Version: 1.1

Licence: Redistributable. Based on  
<https://mail.gna.org/public/eagleusb-dev/2004-11/msg00172.html>

-----  
Driver: vxge - Exar X3100 Series 10GbE PCIe I/O Virtualized Server Adapter

File: vxge/X3fw.ncf  
File: vxge/X3fw-pxe.ncf  
Version: 1.8.1

Licence:

This file contains firmware data derived from proprietary unpublished source code, Copyright (c) 2010 Exar Corporation.

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-----  
Driver: brcmsmac - Broadcom 802.11n softmac wireless LAN driver.

File: brcm/bcm43xx-0.fw  
File: brcm/bcm43xx\_hdr-0.fw  
Version: 610.812

Licence: Redistributable. See LICENCE.broadcom\_bcm43xx for details.

-----  
Driver: brcmfmac - Broadcom 802.11n fullmac wireless LAN driver.

File: brcm/bcm4329-fullmac-4.bin  
File: brcm/brcmfmac43236b.bin  
File: brcm/brcmfmac4329-sdio.bin  
File: brcm/brcmfmac4330-sdio.bin  
File: brcm/brcmfmac4334-sdio.bin  
File: brcm/brcmfmac43340-sdio.bin  
File: brcm/brcmfmac4335-sdio.bin  
File: brcm/brcmfmac43362-sdio.bin  
File: brcm/brcmfmac4339-sdio.bin  
File: brcm/brcmfmac43241b0-sdio.bin  
File: brcm/brcmfmac43241b4-sdio.bin  
File: brcm/brcmfmac43241b5-sdio.bin

File: brcm/brcmfmac43242a.bin  
File: brcm/brcmfmac43143.bin  
File: brcm/brcmfmac43143-sdio.bin  
File: brcm/brcmfmac43430-sdio.bin  
File: brcm/brcmfmac43455-sdio.bin  
File: brcm/brcmfmac4350c2-pcie.bin  
File: brcm/brcmfmac4350-pcie.bin  
File: brcm/brcmfmac4354-sdio.bin  
File: brcm/brcmfmac4356-pcie.bin  
File: brcm/brcmfmac4356-sdio.bin  
File: brcm/brcmfmac43569.bin  
File: brcm/brcmfmac43570-pcie.bin  
File: brcm/brcmfmac4358-pcie.bin  
File: brcm/brcmfmac43602-pcie.bin  
File: brcm/brcmfmac43602-pcie.ap.bin  
File: brcm/brcmfmac4366b-pcie.bin  
File: brcm/brcmfmac4371-pcie.bin

Licence: Redistributable. See LICENCE.broadcom\_bcm43xx for details.

-----  
Driver: tda7706 - FM radio Highly integrated tuner for AM/FM car-radio

File: TDA7706\_OM\_v2.5.1\_boot.txt  
File: TDA7706\_OM\_v3.0.2\_boot.txt

Licence: Redistributable. See LICENCE.tda7706-firmware.txt for details.  
Copyright c 2010 STMicroelectronics

-----  
Driver: wl1251 - Texas Instruments 802.11 WLAN driver for WiLink4 chips

File: ti-connectivity/wl1251-fw.bin  
Version: 4.0.4.3.7

File: ti-connectivity/wl1251-nvs.bin

Licence: Redistributable. See LICENCE.wl1251 for details.

The published NVS files are for testing only. Every device needs to  
have a unique NVS which is properly calibrated for best results.

The driver expects to find the firmwares under a ti-connectivity subdirectory.  
So if your system looks for firmwares in /lib/firmware, the firmwares for  
wl12xx chips must be located in /lib/firmware/ti-connectivity/.

-----  
Driver: wl12xx - Texas Instruments 802.11 WLAN driver for WiLink6/7 chips

File: ti-connectivity/wl1271-fw.bin  
Version: 6.1.0.50.350 (STA-only)  
File: ti-connectivity/wl1271-fw-2.bin  
Version: 6.1.5.50.74 (STA-only)  
File: ti-connectivity/wl1271-fw-ap.bin  
Version: 6.2.1.0.54 (AP-only)  
File: ti-connectivity/wl127x-fw-3.bin  
Version: 6.3.0.0.77

File: ti-connectivity/wl127x-fw-plt-3.bin  
Version: 6.3.0.0.77 (PLT-only)  
File: ti-connectivity/wl127x-fw-4-sr.bin  
Version: 6.3.5.0.98 (Single-role)  
File: ti-connectivity/wl127x-fw-4-mr.bin  
Version: 6.5.2.0.15 (Multi-role)  
File: ti-connectivity/wl127x-fw-4-plt.bin  
Version: 6.3.5.0.98 (PLT-only)  
File: ti-connectivity/wl127x-fw-5-sr.bin  
Version: 6.3.10.0.141 (Single-role)  
File: ti-connectivity/wl127x-fw-5-mr.bin  
Version: 6.5.7.0.49 (Multi-role)  
File: ti-connectivity/wl127x-fw-5-plt.bin  
Version: 6.3.10.0.141 (PLT-only)

File: ti-connectivity/wl128x-fw.bin  
Version: 7.1.5.50.74 (STA-only)  
File: ti-connectivity/wl128x-fw-ap.bin  
Version: 7.2.1.0.54 (AP-only)  
File: ti-connectivity/wl128x-fw-3.bin  
Version: 7.3.0.0.77  
File: ti-connectivity/wl128x-fw-plt-3.bin  
Version: 7.3.0.0.77  
File: ti-connectivity/wl128x-fw-4-sr.bin  
Version: 7.3.5.0.98 (Single-role)  
File: ti-connectivity/wl128x-fw-4-mr.bin  
Version: 7.5.2.0.15 (Multi-role)  
File: ti-connectivity/wl128x-fw-4-plt.bin  
Version: 7.3.5.0.98 (PLT)  
File: ti-connectivity/wl128x-fw-5-sr.bin  
Version: 7.3.10.0.141 (Single-role)  
File: ti-connectivity/wl128x-fw-5-mr.bin  
Version: 7.5.7.0.49 (Multi-role)  
File: ti-connectivity/wl128x-fw-5-plt.bin  
Version: 7.3.10.2.141 (PLT-only)

File: ti-connectivity/wl127x-nvs.bin  
File: ti-connectivity/wl128x-nvs.bin  
Link: ti-connectivity/wl12xx-nvs.bin -> wl127x-nvs.bin  
Link: ti-connectivity/wl1271-nvs.bin -> wl127x-nvs.bin

Licence: Redistributable. See LICENCE.ti-connectivity for details.

The NVS file includes two parts:

- radio calibration
- HW configuration parameters (aka. INI values)

The published NVS files are for testing only. Every device needs to have a unique NVS which is properly calibrated for best results. You can find more information about NVS generation for your device here:

<http://wireless.kernel.org/en/users/Drivers/wl12xx/calibrator>

If you're using a wl127x based device, use a symbolic link called wl1271-nvs.bin that links to the wl127x-nvs.bin file. If you are using wl128x, link to wl128x-nvs.bin instead.

The driver expects to find the firmwares under a ti-connectivity subdirectory. So if your system looks for firmwares in /lib/firmware, the firmwares for wl12xx chips must be located in

/lib/firmware/ti-connectivity/.

---

Driver: wl18xx - Texas Instruments 802.11 WLAN driver for WiLink8 chips

File: ti-connectivity/wl18xx-fw.bin  
Version: 8.2.0.0.100  
File: ti-connectivity/wl18xx-fw-2.bin  
Version: 8.5.0.0.55  
File: ti-connectivity/wl18xx-fw-3.bin  
Version: 8.8.0.0.13  
File: ti-connectivity/wl18xx-fw-4.bin  
Version: 8.9.0.0.69

Licence: Redistributable. See LICENCE.ti-connectivity for details.

The driver expects to find the firmwares under a ti-connectivity subdirectory. So if your system looks for firmwares in /lib/firmware, the firmwares for wl18xx chips must be located in /lib/firmware/ti-connectivity/.

---

Driver: TI\_ST - Texas Instruments bluetooth driver

File: ti-connectivity/TIInit\_7.2.31.bts

Licence: Redistributable. See LICENCE.ti-connectivity for details.

TIInit\_7.2.31.bts version 7.2.31

In order to use that file copy it to /lib/firmware/ti-connectivity.

---

Driver: tlg2300 - Telgent 2300 V4L/DVB driver.

File: tlg2300\_firmware.bin

Licence: Redistributable.

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---

Driver: r8712u - Realtek 802.11n WLAN driver for RTL8712U

File: rtlwifi/rtl8712u.bin  
Info: From Vendor's rtl8712\_8188\_8191\_8192SU\_usb\_linux\_v7\_0.20100831  
Reverted rtl8188C\_8192C\_8192D\_usb\_linux\_v3.4.2\_3727.20120404

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: rtl8192ce - Realtek 802.11n WLAN driver for RTL8192CE

File: rtlwifi/rtl8192cfw.bin  
File: rtlwifi/rtl8192cfwU.bin  
File: rtlwifi/rtl8192cfwU\_B.bin  
Info: From Vendor's realtek/rtlwifi\_linux\_mac80211\_0019.0320.2014V628 driver

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.  
-----

Driver: rtl8192cu - Realtek 802.11n WLAN driver for RTL8192CU

File: rtlwifi/rtl8192cufw.bin  
File: rtlwifi/rtl8192cufw\_A.bin  
File: rtlwifi/rtl8192cufw\_B.bin  
File: rtlwifi/rtl8192cufw\_TMSC.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.  
-----

Driver: rtl8192se - Realtek 802.11n WLAN driver for RTL8192SE

Info: updated from rtl\_92ce\_92se\_92de\_linux\_mac80211\_0004.0816.2011 driver version  
File: rtlwifi/rtl8192sefw.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.  
-----

Driver: rtl8192de - Realtek 802.11n WLAN driver for RTL8192DE

Info: Updated from Realtek version rtl\_92ce\_92se\_92de\_8723ae\_linux\_mac80211\_0007.0809.2012  
File: rtlwifi/rtl8192defw.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.  
-----

Driver: rtl8723e - Realtek 802.11n WLAN driver for RTL8723E

Info: Taken from Realtek version rtl\_92ce\_92se\_92de\_8723ae\_linux\_mac80211\_0007.0809.2012  
File: rtlwifi/rtl8723fw.bin  
File: rtlwifi/rtl8723fw\_B.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.  
-----

Driver: rtl8723be - Realtek 802.11n WLAN driver for RTL8723BE

Info: From Vendor's realtek/rtlwifi\_linux\_mac80211\_0019.0320.2014V628 driver  
File: rtlwifi/rtl8723befw.bin  
Info: Update to version 36 - Sent by Realtek  
File: rtlwifi/rtl8723befw\_36.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: r8723au - Realtek 802.11n WLAN driver for RTL8723AU

Info: Taken from Realtek driver rtl8723A\_WiFi\_linux\_v4.1.3\_6044.20121224

Firmware is embedded in the driver as data statements. This info  
has been extracted into a binary file.

File: rtlwifi/rtl8723aufw\_A.bin

File: rtlwifi/rtl8723aufw\_B.bin

File: rtlwifi/rtl8723aufw\_B\_NoBT.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: rtl8188ee - Realtek 802.11n WLAN driver for RTL8188EE

Info: Taken from Realtek version rtl\_92ce\_92se\_92de\_8723ae\_88ee\_linux\_mac80211\_0010.0109.2013

File: rtlwifi/rtl8188efw.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: rtl8188eu - Realtek 802.11n WLAN driver for RTL8188EU

Info: Taken from Realtek version RTL8188EUS\_linux\_v4.1.4\_6773.20130222

File: rtlwifi/rtl8188eufw.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: rtl8821ae - Realtek 802.11n WLAN driver for RTL8821AE

Info: From Vendor's realtek/rtlwifi\_linux\_mac80211\_0019.0320.2014V628 driver

File: rtlwifi/rtl8821aefw.bin

File: rtlwifi/rtl8821aefw\_wowlan.bin

Info: Update to version 29 - Sent by Realtek

File: rtlwifi/rtl8821aefw\_29.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: rtl8192ee - Realtek 802.11n WLAN driver for RTL8192EE

Info: Initial version taken from Realtek version

rtl\_92ce\_92se\_92de\_8723ae\_88ee\_8723be\_92ee\_linux\_mac80211\_0017.1224.2013

Updated Jan. 14, 2015 with file added by Realtek to

[http://github.com/lwfinger/rtlwifi\\_new.git](http://github.com/lwfinger/rtlwifi_new.git).

File: rtlwifi/rtl8192eefw.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

-----  
Driver: rtl8723bs - Realtek 802.11n WLAN driver for RTL8723BS

Info: Firmware files extracted from data statements in Realtek driver  
v4.3.5.5\_12290.20140916\_BTCOEX20140507-4E40.

File: rtlwifi/rtl8723bs\_bt.bin

File: rtlwifi/rtl8723bs\_nic.bin

File: rtlwifi/rtl8723bs\_ap\_wowlan.bin

File: rtlwifi/rtl8723bs\_wowlan.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

---

Driver: rtl8xxxu - Realtek 802.11n WLAN driver for RTL8XXX USB devices

Info: rtl8723au taken from Realtek driver

rtl8723A\_WiFi\_linux\_v4.1.3\_6044.20121224

Firmware is embedded in the driver as data statements. This info  
has been extracted into a binary file.

File: rtlwifi/rtl8723aufw\_A.bin

File: rtlwifi/rtl8723aufw\_B.bin

File: rtlwifi/rtl8723aufw\_B\_NoBT.bin

Info: rtl8723bu taken from Realtek driver

rtl8723BU\_WiFi\_linux\_v4.3.16\_14189.20150519\_BTCOEX20150119-5844

Firmware is embedded in the driver as data statements. This info  
has been extracted into a binary file.

File: rtlwifi/rtl8723bu\_nic.bin

File: rtlwifi/rtl8723bu\_wowlan.bin

File: rtlwifi/rtl8723bu\_ap\_wowlan.bin

Info: rtl8192eu taken from Realtek driver

rtl8192EU\_linux\_v4.3.1.1\_11320.20140505

Firmware is embedded in the driver as data statements. This info  
has been extracted into a binary file.

File: rtlwifi/rtl8192eu\_nic.bin

File: rtlwifi/rtl8192eu\_wowlan.bin

File: rtlwifi/rtl8192eu\_ap\_wowlan.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

---

Driver: r8169 - RealTek 8169/8168/8101 ethernet driver.

File: rtl\_nic/rtl8168d-1.fw

File: rtl\_nic/rtl8168d-2.fw

File: rtl\_nic/rtl8105e-1.fw

File: rtl\_nic/rtl8168e-1.fw

File: rtl\_nic/rtl8168e-2.fw

File: rtl\_nic/rtl8168e-3.fw

Version: 0.0.4

File: rtl\_nic/rtl8168f-1.fw

Version: 0.0.5

File: rtl\_nic/rtl8168f-2.fw

Version: 0.0.4

File: rtl\_nic/rtl8411-1.fw



Version: 0.0.3

File: rtl\_nic/rtl8411-2.fw  
Version: 0.0.1

File: rtl\_nic/rtl8402-1.fw  
Version: 0.0.1

File: rtl\_nic/rtl8106e-1.fw  
Version: 0.0.1

File: rtl\_nic/rtl8106e-2.fw  
Version: 0.0.1

File: rtl\_nic/rtl8168g-1.fw  
Version: 0.0.3

File: rtl\_nic/rtl8168g-2.fw  
Version: 0.0.1

File: rtl\_nic/rtl8168g-3.fw  
Version: 0.0.1

File: rtl\_nic/rtl8168h-1.fw  
Version: 0.0.2

File: rtl\_nic/rtl8168h-2.fw  
Version: 0.0.2

File: rtl\_nic/rtl8107e-1.fw  
Version: 0.0.2

File: rtl\_nic/rtl8107e-2.fw  
Version: 0.0.2

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- \* notice is accompanying it.

-----  
Driver: vt6656 - VIA VT6656 USB wireless driver

File: vntwusb.fw

Licence: Redistributable. See LICENCE.via\_vt6656 for details.

-----  
Driver: DFU Driver for Atheros bluetooth chipset AR3012

File: ar3k/AthrBT\_0x01020001.dfu  
File: ar3k/ramps\_0x01020001\_26.dfu  
File: ar3k/AthrBT\_0x01020200.dfu  
File: ar3k/ramps\_0x01020200\_26.dfu  
File: ar3k/ramps\_0x01020200\_40.dfu  
File: ar3k/AthrBT\_0x31010000.dfu

File: ar3k/ramps\_0x31010000\_40.dfu  
File: ar3k/AthrBT\_0x11020000.dfu  
File: ar3k/ramps\_0x11020000\_40.dfu  
File: ar3k/ramps\_0x01020201\_26.dfu  
File: ar3k/ramps\_0x01020201\_40.dfu  
File: ar3k/AthrBT\_0x41020000.dfu  
File: ar3k/ramps\_0x41020000\_40.dfu  
File: ar3k/AthrBT\_0x11020100.dfu  
File: ar3k/ramps\_0x11020100\_40.dfu  
File: ar3k/AthrBT\_0x31010100.dfu  
File: ar3k/ramps\_0x31010100\_40.dfu

Licence: Redistributable. See LICENCE.atheros\_firmware for details

-----  
Driver: DFU Driver for Atheros bluetooth chipset AR3012

File: ar3k/AthrBT\_0x01020201.dfu  
File: ar3k/1020201coex/ramps\_0x01020201\_26\_HighPriority.dfu

Licence: Redistributable. See LICENSE.QualcommAtheros\_ar3k for details

-----  
Driver:Atheros AR300x UART HCI Bluetooth Chip driver

File: ar3k/1020201/PS\_ASIC.pst  
File: ar3k/1020201/RamPatch.txt  
File: ar3k/1020200/ar3kbdaddr.pst  
File: ar3k/1020200/PS\_ASIC.pst  
File: ar3k/1020200/RamPatch.txt  
File: ar3k/30101/ar3kbdaddr.pst  
File: ar3k/30101/PS\_ASIC.pst  
File: ar3k/30101/RamPatch.txt  
File: ar3k/30000/ar3kbdaddr.pst  
File: ar3k/30000/PS\_ASIC.pst  
File: ar3k/30000/RamPatch.txt

Licence: Redistributable. See LICENCE.atheros\_firmware for details

-----  
Driver: ath6kl - Atheros support for AR6003

File: ath6k/AR6004/hw1.3/fw-3.bin  
File: ath6k/AR6004/hw1.3/bdata.bin  
File: ath6k/AR6004/hw1.2/fw-2.bin  
File: ath6k/AR6004/hw1.2/bdata.bin  
File: ath6k/AR6003/hw1.0/otp.bin.z77  
File: ath6k/AR6003/hw1.0/bdata.SD31.bin  
File: ath6k/AR6003/hw1.0/bdata.SD32.bin  
File: ath6k/AR6003/hw1.0/data.patch.bin  
File: ath6k/AR6003/hw1.0/bdata.WB31.bin  
File: ath6k/AR6003/hw1.0/athwlan.bin.z77  
File: ath6k/AR6003/hw2.1.1/fw-2.bin  
File: ath6k/AR6003/hw2.1.1/fw-3.bin  
File: ath6k/AR6003/hw2.1.1/otp.bin  
File: ath6k/AR6003/hw2.1.1/athwlan.bin  
File: ath6k/AR6003/hw2.1.1/endpointping.bin

File: ath6k/AR6003/hw2.1.1/bdata.SD31.bin  
File: ath6k/AR6003/hw2.1.1/bdata.SD32.bin  
File: ath6k/AR6003/hw2.1.1/data.patch.bin  
File: ath6k/AR6003/hw2.1.1/bdata.WB31.bin  
File: ath6k/AR6003/hw2.0/otp.bin.z77  
File: ath6k/AR6003/hw2.0/bdata.SD31.bin  
File: ath6k/AR6003/hw2.0/bdata.SD32.bin  
File: ath6k/AR6003/hw2.0/data.patch.bin  
File: ath6k/AR6003/hw2.0/bdata.WB31.bin  
File: ath6k/AR6003/hw2.0/athwlan.bin.z77  
File: ath6k/AR6002/ceprom.data  
File: ath6k/AR6002/ceprom.bin  
File: ath6k/AR6002/athwlan.bin.z77  
File: ath6k/AR6002/data.patch.hw2\_0.bin

Licence: Redistributable. See LICENCE.atheros\_firmware for details

-----  
Driver: ath10k - Qualcomm Atheros support for QCA988x family of chips

File: ath10k/QCA988X/hw2.0/board.bin  
File: ath10k/QCA988X/hw2.0/firmware-4.bin  
Version: 10.2.4.45  
Licence: ath10k/QCA988X/hw2.0/notice\_ath10k\_firmware-4.txt  
File: ath10k/QCA988X/hw2.0/firmware-5.bin  
Version: 10.2.4.70.54  
Licence: ath10k/QCA988X/hw2.0/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA6174/hw2.1/board.bin  
File: ath10k/QCA6174/hw2.1/board-2.bin  
File: ath10k/QCA6174/hw2.1/firmware-5.bin  
Version: SW\_RM.1.1.1-00157-QCARMSWPZ-1  
Licence: ath10k/QCA6174/hw2.1/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA6174/hw3.0/board.bin  
File: ath10k/QCA6174/hw3.0/board-2.bin  
File: ath10k/QCA6174/hw3.0/firmware-4.bin  
Version: WLAN.RM.2.0-00180-QCARMSWPZ-1  
Licence: ath10k/QCA6174/hw3.0/notice\_ath10k\_firmware-4.txt  
File: ath10k/QCA9377/hw1.0/board.bin  
File: ath10k/QCA9377/hw1.0/board-2.bin  
File: ath10k/QCA9377/hw1.0/firmware-5.bin  
Version: WLAN.TF.1.0-00267-1  
Licence: ath10k/QCA9377/hw1.0/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA99X0/hw2.0/board.bin  
File: ath10k/QCA99X0/hw2.0/firmware-5.bin  
Version: 10.4.1.00030-1  
Licence: ath10k/QCA99X0/hw2.0/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA4019/hw1.0/board-2.bin  
File: ath10k/QCA4019/hw1.0/firmware-5.bin  
Version: 10.4-3.2.1-00028  
Licence: ath10k/QCA4019/hw1.0/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA9887/hw1.0/board.bin  
File: ath10k/QCA9887/hw1.0/firmware-5.bin  
Version: 10.2.4-1.0-00013  
Licence: ath10k/QCA9887/hw1.0/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA9888/hw2.0/board-2.bin  
File: ath10k/QCA9888/hw2.0/firmware-5.bin  
Version: 10.4-3.2-00072  
Licence: ath10k/QCA9888/hw2.0/notice\_ath10k\_firmware-5.txt  
File: ath10k/QCA9984/hw1.0/board-2.bin

File: ath10k/QCA9984/hw1.0/firmware-5.bin  
Version: 10.4-3.2-00072  
Licence: ath10k/QCA9984/hw1.0/notice\_ath10k\_firmware-5.txt

Licence: Redistributable. See LICENSE.QualcommAtheros\_ath10k for details

---

Driver: myri10ge - Myri10GE 10GbE NIC driver

File: myri10ge\_eth\_z8e.dat  
File: myri10ge\_ethp\_z8e.dat  
File: myri10ge\_rss\_eth\_z8e.dat  
File: myri10ge\_rss\_ethp\_z8e.dat  
File: myri10ge\_eth\_big\_z8e.dat  
File: myri10ge\_ethp\_big\_z8e.dat  
File: myri10ge\_rss\_eth\_big\_z8e.dat  
File: myri10ge\_rss\_ethp\_big\_z8e.dat  
Version: 1.4.57

License: Redistributable. See LICENSE.myri10ge\_firmware for details.

---

Driver: ath6kl - Atheros support for AR6003 WiFi-Bluetooth combo module

File: ath6k/AR6003.1/hw2.1.1/athwlan.bin  
File: ath6k/AR6003.1/hw2.1.1/bdata.SD31.bin  
File: ath6k/AR6003.1/hw2.1.1/bdata.SD32.bin  
File: ath6k/AR6003.1/hw2.1.1/bdata.WB31.bin  
File: ath6k/AR6003.1/hw2.1.1/data.patch.bin  
File: ath6k/AR6003.1/hw2.1.1/endpointping.bin  
File: ath6k/AR6003.1/hw2.1.1/otp.bin

License: Redistributable. See LICENSE.atheros\_firmware for details

---

Driver: ath6kl - Atheros support for AR3001 WiFi-Bluetooth combo module

File: ar3k/30101coex/ar3kbdaddr.pst  
File: ar3k/30101coex/PS\_ASIC\_aclLowPri.pst  
File: ar3k/30101coex/PS\_ASIC\_aclHighPri.pst  
File: ar3k/30101coex/PS\_ASIC.pst  
File: ar3k/30101coex/RamPatch.txt

License: Redistributable. See LICENSE.atheros\_firmware for details

---

Driver: drxk - Micronas DRX-K demodulator driver

File: dvb-usb-terratec-h5-drxk.fw

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-----  
Driver: ene-ub6250 -- ENE UB6250 SD card reader driver

File: ene-ub6250/sd\_init1.bin  
File: ene-ub6250/sd\_init2.bin  
File: ene-ub6250/sd\_rdwr.bin  
File: ene-ub6250/ms\_init.bin  
File: ene-ub6250/msp\_rdwr.bin  
File: ene-ub6250/ms\_rdwr.bin

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-----  
Driver: isci -- Intel C600 SAS controller driver

File: isci/isci\_firmware.bin  
Source: isci/

Licence: GPLv2. See GPL-2 for details.

-----  
Driver: ar5523 -- Atheros AR5523 based USB Wifi dongles

File: ar5523.bin

Licence: Redistributable. See LICENCE.atheros\_firmware for details

-----  
Driver: s5p-mfc - Samsung MFC video encoder/decoder driver

File: s5p-mfc.fw  
File: s5p-mfc-v6.fw  
File: s5p-mfc-v6-v2.fw  
File: s5p-mfc-v7.fw  
File: s5p-mfc-v8.fw

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-----  
Driver: carl9170 -- Atheros AR9170 802.11 draft-n USB driver

File: carl9170-1.fw  
Version: 1.9.6  
Source: carl9170fw/

Downloaded from <http://linuxwireless.org/en/users/Drivers/carl9170>

Licence: GPLv2. Some build scripts use the New BSD (3-clause) licence.. See GPLv2 for details.

-----  
Driver: snd-hda-codec-ca0132 - Creative Sound Core3D codec

File: ctefx.bin  
File: ctspeq.bin

Licence: Redistributable. See LICENCE.ca0132 for details

Found also in alsa-firmware package.

-----  
Driver: btusb - Bluetooth USB driver

File: intel/ibt-hw-37.7.bseq  
Version: 1316.02.00  
File: intel/ibt-hw-37.7.10-fw-1.80.2.3.d.bseq  
Version: BT\_WilkinsPeak\_B3\_REL\_86\_0001  
File: intel/ibt-hw-37.7.10-fw-1.0.2.3.d.bseq  
Version: BT\_WilkinsPeak\_B3\_REL\_86\_0001  
File: intel/ibt-hw-37.7.10-fw-1.80.1.2d.d.bseq  
Version: BT\_WilkinsPeak\_B5\_REL\_39\_0001  
File: intel/ibt-hw-37.7.10-fw-1.0.1.2d.d.bseq  
Version: BT\_WilkinsPeak\_B5\_REL\_39\_0001  
File: intel/ibt-hw-37.8.bseq  
Version: 1339\_02.00  
File: intel/ibt-hw-37.8.10-fw-1.10.2.27.d.bseq  
Version: BT\_StonePeak\_C0\_REL\_59\_0001  
File: intel/ibt-hw-37.8.10-fw-1.10.3.11.e.bseq  
Version: BT\_StonePeak\_D0\_REL\_37\_0002  
File: intel/ibt-hw-37.8.10-fw-22.50.19.14.f.bseq  
Version: BT\_StonePeak\_D1\_REL\_28\_0001  
File: intel/ibt-11-5.ddc  
Version: LnP/SfP\_REL0370  
File: intel/ibt-11-5.sfi  
Version: BT\_LightningPeak\_REL0370  
File: intel/ibt-12-16.ddc  
Version: BT\_WindStormPeak\_REL0101  
File: intel/ibt-12-16.sfi  
Version: BT\_WindStormPeak\_REL0101

Licence: Redistributable. See LICENCE.ibt\_firmware for details

File: rtl\_bt/rtl8192ee\_fw.bin  
File: rtl\_bt/rtl8192eu\_fw.bin  
File: rtl\_bt/rtl8723a\_fw.bin  
File: rtl\_bt/rtl8723b\_fw.bin  
File: rtl\_bt/rtl8761a\_fw.bin  
File: rtl\_bt/rtl8812ae\_fw.bin  
File: rtl\_bt/rtl8821a\_fw.bin  
File: rtl\_bt/rtl8822b\_fw.bin

File: rtl\_bt/rtl8822b\_config.bin

Licence: Redistributable. See LICENCE.rtlwifi\_firmware.txt for details.

Found in vendor driver, linux\_bt\_usb\_2.11.20140423\_8723be.rar

From [https://github.com/troy-tan/driver\\_store](https://github.com/troy-tan/driver_store)

Files rtl\_bt/rtl8822b\_\* came directly from Realtek. These files are updated on April 14, 2017.

-----  
Driver: btmtdk\_usb - Bluetooth USB driver

File: mt7650.bin

Licence: Redistributable. See LICENCE.ralink\_a\_mEDIATEK\_company\_firmware for details

-----  
Driver: rp2 -- Control RocketPort 2 serial driver

File: rp2.fw

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-----  
Driver: go7007

File: go7007/s2250-1.fw

File: go7007/s2250-2.fw

Link: s2250.fw -> go7007/s2250-2.fw

Link: s2250\_loader.fw -> go7007/s2250-1.fw

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File: go7007/go7007fw.bin

File: go7007/go7007tv.bin

File: go7007/lr192.fw

File: go7007/px-m402u.fw

File: go7007/px-tv402u.fw

File: go7007/wis-startrek.fw

Licence: Redistributable. See LICENCE.go7007 for details

---

Driver: microcode\_amd - AMD CPU Microcode Update Driver for Linux

File: amd-ucode/microcode\_amd.bin

Version: 2013-07-10

File: amd-ucode/microcode\_amd\_fam15h.bin

Version: 2016-03-16

File: amd-ucode/microcode\_amd\_fam16h.bin

Version: 2014-10-28

License: Redistributable. See LICENSE.amd-ucode for details

---

Driver: mxu11x0 - MOXA UPort 11x0 USB Serial hub driver

File: moxa/moxa-1110.fw

File: moxa/moxa-1130.fw

File: moxa/moxa-1131.fw

File: moxa/moxa-1150.fw

File: moxa/moxa-1151.fw

License: Redistributable. See LICENCE.moxa for details

---

Driver: mxuport - MOXA UPort USB Serial hub driver

File: moxa/moxa-1250.fw

File: moxa/moxa-1251.fw

File: moxa/moxa-1410.fw

File: moxa/moxa-1450.fw

File: moxa/moxa-1451.fw

File: moxa/moxa-1613.fw

File: moxa/moxa-1618.fw

File: moxa/moxa-1653.fw

File: moxa/moxa-1658.fw

License: Redistributable. See LICENCE.moxa for details

---

Driver: cw1200 - ST-E CW1100/CW1200 WLAN driver

File: wsm\_22.bin

Version: WSM395

Licence: Redistributable. See LICENCE.cw1200 for details.

File: sdd\_sagrad\_1091\_1098.bin

License:

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(Please note that the actual device firmware is separately licensed)

-----  
Driver: BFA/BNB - QLogic BR-series Adapter FC/FCOE drivers

File: cbfw-3.2.1.1.bin  
File: ctfw-3.2.1.1.bin  
File: ct2fw-3.2.1.1.bin  
File: cbfw-3.2.3.0.bin  
File: ctfw-3.2.3.0.bin  
File: ct2fw-3.2.3.0.bin  
File: cbfw-3.2.5.1.bin  
File: ctfw-3.2.5.1.bin  
File: ct2fw-3.2.5.1.bin

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-----  
Driver: qat - Intel(R) QAT crypto accelerator

File: qat\_895xcc.bin  
File: qat\_895xcc\_mmp.bin  
File: qat\_c3xxx.bin  
File: qat\_c3xxx\_mmp.bin  
File: qat\_c62x.bin  
File: qat\_c62x\_mmp.bin  
Link: qat\_mmp.bin -> qat\_895xcc\_mmp.bin

Licence: Redistributable. See LICENCE.qat\_firmware for details

-----  
Driver: rsi -- Redpine Signals Inc 91x driver

File: rsi\_91x.fw

Licence:

\* Firmware is:

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-----  
Driver: xhci-rcar -- Renesas R-Car Gen2/3 USB 3.0 host controller driver

File: r8a779x\_usb3\_v1.dlmem

File: r8a779x\_usb3\_v2.dlmem

File: r8a779x\_usb3\_v3.dlmem

Licence: Redistributable. See LICENCE.r8a779x\_usb3 for details.

-----  
Driver: snd\_soc\_sst\_acpi

File: intel/fw\_sst\_0f28.bin-48kHz\_i2s\_master

License: Redistributable. See LICENCE.fw\_sst\_0f28 for details

-----  
Driver: as102 - Abilis Systems Single DVB-T Receiver

File: as102\_data1\_st.hex

File: as102\_data2\_st.hex

License: Redistributable. See LICENCE.Abilis for details

-----  
Driver: it9135 -- ITEtech IT913x DVB-T USB driver

File: dvb-usb-it9135-01.fw

File: dvb-usb-it9135-02.fw

Licence: Redistributable. See LICENCE.it913x for details

-----  
Driver: snd\_soc\_sst\_acpi

File: intel/IntcSST2.bin

Version: 8.4.1.77

License: Redistributable. See LICENCE.IntcSST2 for details

-----  
Driver: snd\_intel\_sst\_core

File: intel/fw\_sst\_0f28.bin

File: intel/fw\_sst\_0f28\_ssp0.bin

License: Redistributable. See LICENCE.fw\_sst\_0f28 for details

-----  
Driver: snd\_intel\_sst\_core

File: intel/fw\_sst\_22a8.bin  
Version: 01.0B.02.02

License: Redistributable. See LICENCE.fw\_sst\_0f28 for details

-----  
Driver: snd-soc-skl

File: intel/dsp\_fw\_release\_v827.bin  
Version: 8.20.00.927  
File: intel/dsp\_fw\_release\_v869.bin  
Version: 8.20.00.869  
File: intel/dsp\_fw\_release\_v896.bin  
Version: 8.20.00.896  
File: intel/dsp\_fw\_release\_v927.bin  
Version: 8.20.00.927  
File: intel/dsp\_fw\_release\_v948.bin  
Version: 8.20.00.948  
File: intel/dsp\_fw\_release\_v951.bin  
Version: 8.20.00.951  
File: intel/dsp\_fw\_release\_v958.bin  
Version: 8.20.00.958  
File: intel/dsp\_fw\_release\_v969.bin  
Version: 8.20.00.969  
Link: intel/dsp\_fw\_release.bin -> intel/dsp\_fw\_release\_v969.bin

License: Redistributable. See LICENCE.adsp\_sst for details

File: intel/dsp\_fw\_bxt\_n\_v430.bin  
Version: 9.00.00.430  
File: intel/dsp\_fw\_bxt\_n\_v702.bin  
Version: 9.00.00.702  
File: intel/dsp\_fw\_bxt\_n\_v1118.bin  
Version: 9.22.00.1118  
File: intel/dsp\_fw\_bxt\_n\_v2010.bin  
Version: 9.22.01.2010\_44  
File: intel/dsp\_fw\_bxt\_n\_v2219.bin  
Version: 9.22.01.2219\_64  
Link: intel/dsp\_fw\_bxt\_n.bin -> intel/dsp\_fw\_bxt\_n\_v2219.bin

License: Redistributable. See LICENCE.adsp\_sst for details

File: intel/dsp\_fw\_kbl\_v701.bin  
Version: 9.21.00.701  
File: intel/dsp\_fw\_kbl\_v1037.bin  
Version: 09.21.00.1037  
Link: intel/dsp\_fw\_kbl.bin -> dsp\_fw\_kbl\_v1037.bin

License: Redistributable. See LICENCE.adsp\_sst for details

File: intel/dsp\_fw\_glk\_v1814.bin  
Version: 9.92.01.1814

Link: intel/dsp\_fw\_glk.bin -> dsp\_fw\_glk\_v1814.bin

License: Redistributable. See LICENCE.adsp\_sst for details

---

Driver: smsmdtv - Siano MDTV Core module

File: cmmb\_vega\_12mhz.inp  
File: cmmb\_venice\_12mhz.inp  
File: dvb\_nova\_12mhz.inp  
File: dvb\_nova\_12mhz\_b0.inp  
File: isdbt\_nova\_12mhz.inp  
File: isdbt\_nova\_12mhz\_b0.inp  
File: isdbt\_rio.inp  
File: smslxxx-hcw-55xxx-dvbt-02.fw  
File: smslxxx-hcw-55xxx-isdbt-02.fw  
File: smslxxx-nova-a-dvbt-01.fw  
File: smslxxx-nova-b-dvbt-01.fw  
File: smslxxx-stellar-dvbt-01.fw  
File: tdmn\_nova\_12mhz.inp

Licence: Redistributable. See LICENCE.siano for details

---

Driver: xhci-tegra -- NVIDIA Tegra XHCI driver

File: nvidia/tegra124/xusb.bin  
Version: v45.46

File: nvidia/tegra210/xusb.bin  
Version: v50.10

Licence: Redistributable. See LICENCE.nvidia for details

---

Driver: atusb - ATUSB IEEE 802.15.4 transceiver driver

File: atusb/atusb-0.2.dfu  
Version: 0.2  
File: atusb/atusb-0.3.dfu  
Version: 0.3  
File: atusb/rzusb-0.3.bin  
Version: 0.3  
Info: atusb/ChangeLog

Licence: GPLv2 or later. See GPLv2 and GPLv3 for details.

---

Driver: qca - Qualcomm Atheros Bluetooth support for QCA61x4 chips

File: qca/nvm\_usb\_00000201.bin  
File: qca/nvm\_usb\_00000200.bin  
File: qca/nvm\_usb\_00000300.bin  
File: qca/nvm\_usb\_00000302.bin  
File: qca/nvm\_00130300.bin  
File: qca/nvm\_00130302.bin

File: qca/rampatch\_usb\_00000200.bin  
File: qca/rampatch\_usb\_00000201.bin  
File: qca/rampatch\_usb\_00000300.bin  
File: qca/rampatch\_usb\_00000302.bin  
File: qca/rampatch\_00130300.bin  
File: qca/rampatch\_00130302.bin

Licence: Redistributable. See LICENSE.QualcommAtheros\_ath10k and qca/NOTICE.txt for details

-----

Driver: liquidio -- Cavium LiquidIO driver

File: liquidio/lio\_23xx\_nic.bin  
Version: v1.5.1

File: liquidio/lio\_210nv\_nic.bin  
Version: v1.5.1

File: liquidio/lio\_210sv\_nic.bin  
Version: v1.5.1

File: liquidio/lio\_410nv\_nic.bin  
Version: v1.5.1

Licence: Redistributable. See LICENCE.cavium for details

-----

Driver: i915 -- Intel Integrated Graphics driver

File: i915/skl\_dmc\_ver1\_23.bin  
File: i915/skl\_dmc\_ver1\_26.bin  
File: i915/skl\_dmc\_ver1.bin  
Version: DMC API/ABI ver 1 - release 26 for Skylake

File: i915/bxt\_dmc\_ver1\_07.bin  
File: i915/bxt\_dmc\_ver1.bin  
Version: DMC API/ABI ver 1 - release 07 for Broxton

File: i915/kbl\_dmc\_ver1\_01.bin  
File: i915/kbl\_dmc\_ver1.bin  
Version: DMC API/ABI ver 1 - release 01 for Kabylake

File: i915/skl\_guc\_ver1.bin  
Version: Guc API/ABI ver 1 - release 1059 for Skylake

File: i915/skl\_guc\_ver4.bin  
Version: Guc API/ABI ver 4 - release 3 for Skylake

File: i915/skl\_guc\_ver6\_1.bin  
File: i915/skl\_guc\_ver6.bin  
Version: Guc API/ABI ver 6 - release 1 for Skylake

File: i915/skl\_huc\_ver01\_07\_1398.bin  
Version: HuC API/ABI ver 1 - release 7 for Skylake

File: i915/bxt\_huc\_ver01\_07\_1398.bin  
Version: HuC API/APB ver 1 - release 7 for Broxton

File: i915/kbl\_huc\_ver02\_00\_1810.bin  
Version: HuC API/APB ver 2 - for Kabylake

File: i915/bxt\_guc\_ver8\_7.bin  
Version: Guc API/APB ver 8 - release 7 for Broxton

File: i915/kbl\_guc\_ver9\_14.bin  
Version: Guc API/APB ver 9 release 14 for Kabylake

License: Redistributable. See LICENSE.i915 for details

-----  
Driver: nouveau - NVIDIA GPU driver

File: nvidia/gk20a/fecs\_data.bin  
File: nvidia/gk20a/fecs\_inst.bin  
File: nvidia/gk20a/gpccs\_data.bin  
File: nvidia/gk20a/gpccs\_inst.bin  
File: nvidia/gk20a/sw\_bundle\_init.bin  
File: nvidia/gk20a/sw\_ctx.bin  
File: nvidia/gk20a/sw\_method\_init.bin  
File: nvidia/gk20a/sw\_nonctx.bin  
File: nvidia/gm200/acr/bl.bin  
File: nvidia/gm200/acr/ucode\_load.bin  
File: nvidia/gm200/acr/ucode\_unload.bin  
File: nvidia/gm200/gr/fecs\_bl.bin  
File: nvidia/gm200/gr/fecs\_data.bin  
File: nvidia/gm200/gr/fecs\_inst.bin  
File: nvidia/gm200/gr/fecs\_sig.bin  
File: nvidia/gm200/gr/gpccs\_bl.bin  
File: nvidia/gm200/gr/gpccs\_data.bin  
File: nvidia/gm200/gr/gpccs\_inst.bin  
File: nvidia/gm200/gr/gpccs\_sig.bin  
File: nvidia/gm200/gr/sw\_bundle\_init.bin  
File: nvidia/gm200/gr/sw\_ctx.bin  
File: nvidia/gm200/gr/sw\_method\_init.bin  
File: nvidia/gm200/gr/sw\_nonctx.bin  
File: nvidia/gm204/acr/bl.bin  
File: nvidia/gm204/acr/ucode\_load.bin  
File: nvidia/gm204/acr/ucode\_unload.bin  
File: nvidia/gm204/gr/fecs\_bl.bin  
File: nvidia/gm204/gr/fecs\_data.bin  
File: nvidia/gm204/gr/fecs\_inst.bin  
File: nvidia/gm204/gr/fecs\_sig.bin  
File: nvidia/gm204/gr/gpccs\_bl.bin  
File: nvidia/gm204/gr/gpccs\_data.bin  
File: nvidia/gm204/gr/gpccs\_inst.bin  
File: nvidia/gm204/gr/gpccs\_sig.bin  
File: nvidia/gm204/gr/sw\_bundle\_init.bin  
File: nvidia/gm204/gr/sw\_ctx.bin  
File: nvidia/gm204/gr/sw\_method\_init.bin  
File: nvidia/gm204/gr/sw\_nonctx.bin  
File: nvidia/gm206/acr/bl.bin  
File: nvidia/gm206/acr/ucode\_load.bin  
File: nvidia/gm206/acr/ucode\_unload.bin  
File: nvidia/gm206/gr/fecs\_bl.bin  
File: nvidia/gm206/gr/fecs\_data.bin  
File: nvidia/gm206/gr/fecs\_inst.bin  
File: nvidia/gm206/gr/fecs\_sig.bin

File: nvidia/gm206/gr/gpccs\_bl.bin  
File: nvidia/gm206/gr/gpccs\_data.bin  
File: nvidia/gm206/gr/gpccs\_inst.bin  
File: nvidia/gm206/gr/gpccs\_sig.bin  
File: nvidia/gm206/gr/sw\_bundle\_init.bin  
File: nvidia/gm206/gr/sw\_ctx.bin  
File: nvidia/gm206/gr/sw\_method\_init.bin  
File: nvidia/gm206/gr/sw\_nonctx.bin  
File: nvidia/gm20b/acr/bl.bin  
File: nvidia/gm20b/acr/ucode\_load.bin  
File: nvidia/gm20b/gr/fecs\_bl.bin  
File: nvidia/gm20b/gr/fecs\_data.bin  
File: nvidia/gm20b/gr/fecs\_inst.bin  
File: nvidia/gm20b/gr/fecs\_sig.bin  
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File: nvidia/gm20b/gr/gpccs\_inst.bin  
File: nvidia/gm20b/gr/sw\_bundle\_init.bin  
File: nvidia/gm20b/gr/sw\_ctx.bin  
File: nvidia/gm20b/gr/sw\_method\_init.bin  
File: nvidia/gm20b/gr/sw\_nonctx.bin  
File: nvidia/gm20b/pmu/desc.bin  
File: nvidia/gm20b/pmu/image.bin  
File: nvidia/gm20b/pmu/sig.bin  
File: nvidia/gp100/acr/bl.bin  
File: nvidia/gp100/acr/ucode\_load.bin  
File: nvidia/gp100/acr/ucode\_unload.bin  
File: nvidia/gp100/gr/fecs\_bl.bin  
File: nvidia/gp100/gr/fecs\_data.bin  
File: nvidia/gp100/gr/fecs\_inst.bin  
File: nvidia/gp100/gr/fecs\_sig.bin  
File: nvidia/gp100/gr/gpccs\_bl.bin  
File: nvidia/gp100/gr/gpccs\_data.bin  
File: nvidia/gp100/gr/gpccs\_inst.bin  
File: nvidia/gp100/gr/gpccs\_sig.bin  
File: nvidia/gp100/gr/sw\_bundle\_init.bin  
File: nvidia/gp100/gr/sw\_ctx.bin  
File: nvidia/gp100/gr/sw\_method\_init.bin  
File: nvidia/gp100/gr/sw\_nonctx.bin  
File: nvidia/gp102/acr/bl.bin  
File: nvidia/gp102/acr/ucode\_load.bin  
File: nvidia/gp102/acr/ucode\_unload.bin  
File: nvidia/gp102/acr/unload\_bl.bin  
File: nvidia/gp102/gr/fecs\_bl.bin  
File: nvidia/gp102/gr/fecs\_data.bin  
File: nvidia/gp102/gr/fecs\_inst.bin  
File: nvidia/gp102/gr/fecs\_sig.bin  
File: nvidia/gp102/gr/gpccs\_bl.bin  
File: nvidia/gp102/gr/gpccs\_data.bin  
File: nvidia/gp102/gr/gpccs\_inst.bin  
File: nvidia/gp102/gr/gpccs\_sig.bin  
File: nvidia/gp102/gr/sw\_bundle\_init.bin  
File: nvidia/gp102/gr/sw\_ctx.bin  
File: nvidia/gp102/gr/sw\_method\_init.bin  
File: nvidia/gp102/gr/sw\_nonctx.bin  
File: nvidia/gp102/nvdec/scrubber.bin  
File: nvidia/gp102/sec2/desc.bin  
File: nvidia/gp102/sec2/image.bin  
File: nvidia/gp102/sec2/sig.bin  
File: nvidia/gp104/acr/bl.bin  
File: nvidia/gp104/acr/ucode\_load.bin

File: nvidia/gp104/acr/ucode\_unload.bin  
File: nvidia/gp104/acr/unload\_bl.bin  
File: nvidia/gp104/gr/fecs\_bl.bin  
File: nvidia/gp104/gr/fecs\_data.bin  
File: nvidia/gp104/gr/fecs\_inst.bin  
File: nvidia/gp104/gr/fecs\_sig.bin  
File: nvidia/gp104/gr/gpccs\_bl.bin  
File: nvidia/gp104/gr/gpccs\_data.bin  
File: nvidia/gp104/gr/gpccs\_inst.bin  
File: nvidia/gp104/gr/gpccs\_sig.bin  
File: nvidia/gp104/gr/sw\_bundle\_init.bin  
File: nvidia/gp104/gr/sw\_ctx.bin  
File: nvidia/gp104/gr/sw\_method\_init.bin  
File: nvidia/gp104/gr/sw\_nonctx.bin  
File: nvidia/gp104/nvdec/scrubber.bin  
File: nvidia/gp104/sec2/desc.bin  
File: nvidia/gp104/sec2/image.bin  
File: nvidia/gp104/sec2/sig.bin  
File: nvidia/gp106/acr/bl.bin  
File: nvidia/gp106/acr/ucode\_load.bin  
File: nvidia/gp106/acr/ucode\_unload.bin  
File: nvidia/gp106/acr/unload\_bl.bin  
File: nvidia/gp106/gr/fecs\_bl.bin  
File: nvidia/gp106/gr/fecs\_data.bin  
File: nvidia/gp106/gr/fecs\_inst.bin  
File: nvidia/gp106/gr/fecs\_sig.bin  
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File: nvidia/gp106/gr/gpccs\_sig.bin  
File: nvidia/gp106/gr/sw\_bundle\_init.bin  
File: nvidia/gp106/gr/sw\_ctx.bin  
File: nvidia/gp106/gr/sw\_method\_init.bin  
File: nvidia/gp106/gr/sw\_nonctx.bin  
File: nvidia/gp106/nvdec/scrubber.bin  
File: nvidia/gp106/sec2/desc.bin  
File: nvidia/gp106/sec2/image.bin  
File: nvidia/gp106/sec2/sig.bin  
File: nvidia/gp107/acr/bl.bin  
File: nvidia/gp107/acr/ucode\_load.bin  
File: nvidia/gp107/acr/ucode\_unload.bin  
File: nvidia/gp107/acr/unload\_bl.bin  
File: nvidia/gp107/gr/fecs\_bl.bin  
File: nvidia/gp107/gr/fecs\_data.bin  
File: nvidia/gp107/gr/fecs\_inst.bin  
File: nvidia/gp107/gr/fecs\_sig.bin  
File: nvidia/gp107/gr/gpccs\_bl.bin  
File: nvidia/gp107/gr/gpccs\_data.bin  
File: nvidia/gp107/gr/gpccs\_inst.bin  
File: nvidia/gp107/gr/gpccs\_sig.bin  
File: nvidia/gp107/gr/sw\_bundle\_init.bin  
File: nvidia/gp107/gr/sw\_ctx.bin  
File: nvidia/gp107/gr/sw\_method\_init.bin  
File: nvidia/gp107/gr/sw\_nonctx.bin  
File: nvidia/gp107/nvdec/scrubber.bin  
File: nvidia/gp107/sec2/desc.bin  
File: nvidia/gp107/sec2/image.bin  
File: nvidia/gp107/sec2/sig.bin  
File: nvidia/gp10b/acr/bl.bin  
File: nvidia/gp10b/acr/ucode\_load.bin



File: nvidia/gp10b/gr/fecs\_bl.bin  
File: nvidia/gp10b/gr/fecs\_data.bin  
File: nvidia/gp10b/gr/fecs\_inst.bin  
File: nvidia/gp10b/gr/fecs\_sig.bin  
File: nvidia/gp10b/gr/gpccs\_bl.bin  
File: nvidia/gp10b/gr/gpccs\_data.bin  
File: nvidia/gp10b/gr/gpccs\_inst.bin  
File: nvidia/gp10b/gr/gpccs\_sig.bin  
File: nvidia/gp10b/gr/sw\_bundle\_init.bin  
File: nvidia/gp10b/gr/sw\_ctx.bin  
File: nvidia/gp10b/gr/sw\_method\_init.bin  
File: nvidia/gp10b/gr/sw\_nonctx.bin  
File: nvidia/gp10b/pmu/desc.bin  
File: nvidia/gp10b/pmu/image.bin  
File: nvidia/gp10b/pmu/sig.bin

Licence: Redistributable. See LICENCE.nvidia for details

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Driver: wilc1000 - Atmel 802.11n WLAN driver for WILC1000

File: atmel/wilc1000\_fw.bin  
File: atmel/wilc1000\_ap\_fw.bin  
File: atmel/wilc1000\_p2p\_fw.bin

License: Redistributable. See LICENSE.atmel for details

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Driver: hfi1 - Intel OPA Gen 1 adapter

File: hfi1\_dc8051.fw  
Version: 0.47  
File: hfi1\_fabric.fw  
Version: 0x1055  
File: hfi1\_pcie.fw  
Version: 0x4755  
File: hfi1\_sbus.fw  
Version: 0x10130001  
File: hfi1\_platform.dat

Licence: Redistributable. See LICENSE.hfi1\_firmware for details

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Driver: knav\_qmss\_queue - TI Keystone 2 QMSS driver

File: ti-keystone/ks2\_qmss\_pdsp\_acc48\_k2\_le\_1\_0\_0\_9.bin

Licence: Redistributable. See LICENCE.ti-keystone for details.

-----

Driver: mwlwifi - Marvell mac80211 driver for 80211ac cards.

File: mwlwifi/88W8864.bin  
Version: 7.2.8.6

File: mwlwifi/88W8897.bin  
Version: 8.2.0.10

Licence: Redistributable. See LICENCE.Marvell for details.

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Driver: mtk-vpu - Mediatek VPU video processing unit driver

File: vpu\_d.bin  
File: vpu\_p.bin

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Driver: rk3399-dptx - ROCKCHIP rk3399 dptx firmware

File: rockchip/dptx.bin  
Version: 3.1

Licence: Redistributable. See LICENCE.rockchip for details.

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Driver: mt76x2e - MediaTek MT76x2 Wireless MACs

File: mt7662.bin  
Version: 1.9

File: mt7662\_rom\_patch.bin  
Version: 0.0.2\_P69

Licence: Redistributable. See LICENCE.ralink\_a\_mediatek\_company\_firmware for details

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Driver: nfp - Netronome Flow Processor

File: netronome/nic\_AMDAA0081-0001\_1x40.nffw  
File: netronome/nic\_AMDAA0097-0001\_2x40.nffw  
File: netronome/nic\_AMDAA0099-0001\_2x10.nffw  
File: netronome/nic\_AMDAA0081-0001\_4x10.nffw  
File: netronome/nic\_AMDAA0097-0001\_4x10\_1x40.nffw  
File: netronome/nic\_AMDAA0099-0001\_2x25.nffw  
File: netronome/nic\_AMDAA0096-0001\_2x10.nffw  
File: netronome/nic\_AMDAA0097-0001\_8x10.nffw

Version: v0.6.1

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#### NOTICES:

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1.

/\*

\* FILE: sha2.c

\* AUTHOR: Aaron D. Gifford <me@aarongifford.com>

\*

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\*

\* \$Id: sha2.c,v 1.1 2001/11/08 00:01:51 adg Exp adg \$  
\*/

File: LLM\_sp\_sha2.c

/\*

\* FILE: sha2.h

\* AUTHOR: Aaron D. Gifford <me@aarongifford.com>

\*

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\*

\* \$Id: sha2.h,v 1.1 2001/11/08 00:02:01 adg Exp adg \$

\*/

File: LLM\_sp\_sha2.h

2.

/\* utility to create the register check tables

\* this includes inlined list.h safe for userspace.

\*

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\*

\* Authors:

\* Jerome Glisse

\* Dave Airlie

\*/

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\*/

File: comm\_lib.h

3.

/\* crypto/aes/aes.h -\*- mode:C; c-file-style: "eay" -\*- \*/

/\*

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\* =====  
\*  
\*/

File Name list: aes.h, aes\_locl.h

4.

/\* crypto/aes/aes\_core.c -\*- mode:C; c-file-style: "eay" -\*- \*/  
/\*\*  
\* rijndael-alg-fst.c  
\*  
\* @version 3.0 (December 2000)  
\*  
\* Optimised ANSI C code for the Rijndael cipher (now AES)  
\*  
\* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>  
\* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>  
\* @author Paulo Barreto <paulo.barreto@terra.com.br>  
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File: aes\_core.c

5.

/\* =====

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\$Header: //depot/asic/msmshared/sec/sec/MSM\_SEC.03.03.08.xx/aes.h#1 \$  
\$DateTime: 2006/12/11 00:43:21 \$ \$Author: davidf \$

when who what, where, why

----- --

11/12/04 rv changes to correct LINT errors  
02/02/04 rwh Small mods to open source.

=====\*/

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Dr B. R. Gladman <brg@gladman.uk.net> 1st June 2001.

This is an implementation of the AES encryption algorithm (Rijndael) designed by Joan Daemen and Vincent Rijmen. This version is designed to provide both fixed and dynamic block and key lengths and can also run with either big or little endian internal byte order.

NOTE: Input block and key lengths are given in terms of the lengths of the byte arrays involved, the legal values being 16, 24 and 32.

#### A. THE CIPHER INTERFACE

byte (an unsigned 8-bit type)  
word (an unsigned 32-bit type)  
aes\_ret: (a signed 16 bit type for function return values)  
aes\_good (value != 0, a good return)  
aes\_bad (value == 0, an error return)  
enum aes\_key: (encryption direction)  
enc (set key for encryption)  
dec (set key for decryption)  
both (set key for both)  
class or struct aes (structure for context)

C subroutine calls:

aes\_ret set\_blk(const word block\_length, aes \*cx) (variable block size)  
aes\_ret set\_key(const byte key[], const word key\_length,  
const enum aes\_key direction, aes \*cx)  
aes\_ret encrypt(const byte input\_blk[], byte output\_blk[], const aes \*cx)  
aes\_ret decrypt(const byte input\_blk[], byte output\_blk[], const aes \*cx)

IMPORTANT NOTE: If you are using this C interface and your compiler does not set the memory used for objects to zero before use, you will need to ensure that cx.mode is set to zero before using the C subroutine calls.

C++ aes class subroutines:

aes\_ret set\_blk(const word block\_length) (variable block size)  
aes\_ret set\_key(const byte key[], const word key\_length,  
const aes\_key direction)  
aes\_ret encrypt(const byte input\_blk[], byte output\_blk[]) const  
aes\_ret decrypt(const byte input\_blk[], byte output\_blk[]) const

The block length inputs to set\_block and set\_key are in numbers of BYTES, not bits. The calls to subroutines must be made in the above order but multiple calls can be made without repeating earlier calls if their parameters have not changed. If the cipher block length is

variable but `set_blk` has not been called before cipher operations a value of 16 is assumed (that is, the AES block size). In contrast to earlier versions the block and key length parameters are now checked for correctness and the encryption and decryption routines check to ensure that an appropriate key has been set before they are called.

## B. BYTE ORDER WITHIN 32 BIT WORDS

The fundamental data processing units in Rijndael are 8-bit bytes. The input, the output and the key input are all enumerated arrays of bytes in which bytes are numbered starting at zero and increasing to one less than the number of bytes in the array in question. When these inputs and outputs are considered as bit sequences, the  $n$ 'th byte contains bits  $8n$  to  $8n+7$  of the sequence with the lower numbered bit mapped to the most significant bit within the byte (i.e. that having a numeric value of 128). However, Rijndael can be implemented more efficiently using 32-bit words to process 4 bytes at a time provided that the order of bytes within words is known. This order is called big-endian if the lowest numbered bytes in words have the highest numeric significance and little-endian if the opposite applies. This code can work in either order irrespective of the native order of the machine on which it runs. The byte order used internally is set by defining `INTERNAL_BYTE_ORDER` whereas the order for all inputs and outputs is specified by defining `EXTERNAL_BYTE_ORDER`, the only purpose of the latter being to determine if a byte order change is needed immediately after input and immediately before output to account for the use of a different internal byte order. In almost all situations both of these defines will be set to the native order of the processor on which the code is to run but other settings may sometimes be useful in special circumstances.

```
#define INTERNAL_BYTE_ORDER LITTLE_ENDIAN
#define EXTERNAL_BYTE_ORDER LITTLE_ENDIAN
```

## C. COMPILATION

To compile AES (Rijndael) for use in C code

- a. Exclude the `AES_DLL` define in `aes.h`
- b. Exclude the `AES_IN_CPP` define in `aes.h`

To compile AES (Rijndael) for use in C++ code

- a. Exclude the `AES_DLL` define in `aes.h`
- b. Include the `AES_IN_CPP` define in `aes.h`

To compile AES (Rijndael) in C as a Dynamic Link Library

- a. Include the `AES_DLL` define in `aes.h`
- b. Compile the DLL. If using the test files, exclude `aes.c` from the test build project and compile it with the same defines as used for the DLL (ensure that the DLL path is correct)

## D. CONFIGURATION OPTIONS (see also `aes.c`)

1. define `BLOCK_SIZE` to set the cipher block size (16, 24 or 32) or leave this undefined for dynamically variable block size (this will result in much slower code).
2. set `AES_IN_CPP` to use the code from C++ rather than C
3. set `AES_DLL` if AES (Rijndael) is to be compiled to a DLL
4. set `INTERNAL_BYTE_ORDER` to one of the above constants to set the internal byte order (the order used within the algorithm code)
5. set `EXTERNAL_BYTE_ORDER` to one of the above constants to set the byte order used at the external interfaces for the input, output and key



byte arrays.

IMPORTANT NOTE: BLOCK\_SIZE is in BYTES: 16, 24, 32 or undefined for aes.c  
and 16, 20, 24, 28, 32 or undefined for aes++.c. If left undefined a  
slower version providing variable block length is compiled

```
#define BLOCK_SIZE 16
```

Define AES\_IN\_CPP if you intend to use the AES C++ class rather than the  
C code directly.

```
#define AES_IN_CPP
```

Define AES\_DLL if you wish to compile the code to produce a Windows DLL

```
#define AES_DLL
```

```
*/
```

File: aes.h

```
/*=====
```

#### EDIT HISTORY FOR FILE

\$Header: //depot/asic/msmshared/sec/sec/MSM\_SEC.03.03.08.xx/aes\_tab.h#1 \$  
\$DateTime: 2006/12/11 00:43:21 \$ \$Author: davidf \$

when	who	what, where, why
------	-----	------------------

11/12/04	rv	changes to correct LINT errors
----------	----	--------------------------------

02/02/04	rw	Small mods to open source.
----------	----	----------------------------

```
=====*/
```

```
/*lint -e146 -e303 */
```

```
/* 146: Assuming a binary constant */
```

```
/* 303: String too long (try +macros) */
```

```
/*
```

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```
*/
```

File: aes\_tab.h

```
=====
```

6.

```
=====
```

```
#FILE:Confest.py
```

```
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```

```
# Copyright (c) 2001, 2002, 2003 Steven Knight
```

```
#
```

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#

#
# The purpose of this module is to define how a check is to be performed.
# Use one of the Check...() functions below.
#

#
# A context class is used that defines functions for carrying out the tests,
# logging and messages. The following methods and members must be present:
#
# context.Display(msg) Function called to print messages that are normally
#                       displayed for the user. Newlines are explicitly used.
#                       The text should also be written to the logfile!
#
# context.Log(msg)      Function called to write to a log file.
#
# context.BuildProg(text, ext)
#                       Function called to build a program, using "ext" for the
#                       file extension. Must return an empty string for
#                       success, an error message for failure.
#                       For reliable test results building should be done just
#                       like an actual program would be build, using the same
#                       command and arguments (including configure results so
#                       far).
#
# context.CompileProg(text, ext)
#                       Function called to compile a program, using "ext" for
#                       the file extension. Must return an empty string for
#                       success, an error message for failure.
#                       For reliable test results compiling should be done just
#                       like an actual source file would be compiled, using the
#                       same command and arguments (including configure results
#                       so far).
#
# context.AppendLIBS(lib_name_list)
#                       Append "lib_name_list" to the value of LIBS.
#                       "lib_namelist" is a list of strings.
#                       Return the value of LIBS before changing it (any type
#                       can be used, it is passed to SetLIBS() later.)
#
# context.PrependLIBS(lib_name_list)
#                       Prepend "lib_name_list" to the value of LIBS.
#                       "lib_namelist" is a list of strings.
#                       Return the value of LIBS before changing it (any type

```

```

#           can be used, it is passed to SetLIBS() later.)
#
# context.SetLIBS(value)
#           Set LIBS to "value". The type of "value" is what
#           AppendLIBS() returned.
#           Return the value of LIBS before changing it (any type
#           can be used, it is passed to SetLIBS() later.)
#
# context.headerfilename
#           Name of file to append configure results to, usually
#           "confdefs.h".
#           The file must not exist or be empty when starting.
#           Empty or None to skip this (some tests will not work!).
#
# context.config_h    (may be missing). If present, must be a string, which
#           will be filled with the contents of a config_h file.
#
# context.vardict      Dictionary holding variables used for the tests and
#           stores results from the tests, used for the build
#           commands.
#           Normally contains "CC", "LIBS", "CPPFLAGS", etc.
#
# context.havedict     Dictionary holding results from the tests that are to
#           be used inside a program.
#           Names often start with "HAVE_". These are zero
#           (feature not present) or one (feature present). Other
#           variables may have any value, e.g., "PERLVERSION" can
#

```

```

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* ===== */
=====
```

```

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 *
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=====  
=====  
  
=====  
=====

/\*  
\* AES-based functions  
\*  
\*  
\* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)  
\* - One-Key CBC MAC (OMAC1) hash with AES-128  
\* - AES-128 CTR mode encryption  
\* - AES-128 EAX mode encryption/decryption  
\* - AES-128 CBC  
\*  
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=====  
  
=====  
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```

=====
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/*  
* $DateTime: 2013/07/24 11:35:54 $  
*/  
  
/* $OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp $ */  
/* $NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp $ */  
  
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/*
* $DateTime: 2011/01/07 17:50:19 $
*/

/* $OpenBSD: strcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp $ */

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 // @(#)queue.h 8.5 (Berkeley) 8/20/94  
 // \$FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp \$  
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\*  
\* Modifications to public domain implementation:  
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\* - cleanup  
\* - use C pre-processor to make it easier to change S table access  
\* - added option (AES\_SMALL\_TABLES) for reducing code size by about 8 kB at  
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 * rijndael-alg-fst.c
 *
 * @version 3.0 (December 2000)
 *
 * Optimised ANSI C code for the Rijndael cipher (now AES)
 *
 * @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
 * @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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\* WPA Supplicant / wrapper functions for crypto libraries  
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\* This file defines the cryptographic functions that need to be implemented  
\* for wpa\_supplicant and hostapd. When TLS is not used, internal  
\* implementation of MD5, SHA1, and AES is used and no external libraries are  
\* required. When TLS is enabled (e.g., by enabling EAP-TLS or EAP-PEAP), the  
\* crypto library used by the TLS implementation is expected to be used for  
\* non-TLS needs, too, in order to save space by not implementing these  
\* functions twice.  
\*  
\* Wrapper code for using each crypto library is in its own file (crypto\*.c)  
\* and one of these files is build and linked in to provide the functions

\* defined here.  
\*/

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//
//
// This code implements the MD5 message-digest algorithm.
// The algorithm is due to Ron Rivest. This code was
// written by Colin Plumb in 1993, no copyright is claimed.
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//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
// except that you don't need to include two pages of legalese
// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
```

```
/*=====
@file secrsa_encryption.c

This file provides RSA PKCS #1 v1.5 encryption/decryption implementation.
=====
```

```
EDIT HISTORY FOR FILE
$DateTime: 2013/08/06 21:06:51 $
$Author: pdhaval $
```

```
when    who    what, where, why
-----
04/11/11  qxu    initial version
```

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//
// Name:      lzuncompr.c
// Author:    Vitaly Luban
// Based on code from Marcus Geelnard
// Description: LZ77 decoder implementation.
// Reentrant: Yes
//
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// marcus.geelnard at home.se
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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/nan/sha2.c

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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/romexport/AR6320/hw.3/include/sha2.h

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* This file defines the cryptographic functions that need to be implemented
* for wpa_supplicant and hostapd. When TLS is not used, internal
* implementation of MD5, SHA1, and AES is used and no external libraries are
* required. When TLS is enabled (e.g., by enabling EAP-TLS or EAP-PEAP), the
* crypto library used by the TLS implementation is expected to be used for
* non-TLS needs, too, in order to save space by not implementing these
* functions twice.
*
* Wrapper code for using each crypto library is in its own file (crypto*.c)
* and one of these files is build and linked in to provide the functions
* defined here.
*/
p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/crypto.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/config.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/suppl_auth_api.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/wpa.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/wpa_i.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw\_protocol\_cmn.cnss/1.0.0/src/supplicant/sha256.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw\_protocol\_cmn.cnss/1.0.0/src/supplicant/mywpa\_ft.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/sha256.c

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/mywpa_ft.c

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/mywpa_common.h

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/*
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```

* code or tables extracted from it, as desired without restriction.
*
* First, the polynomial itself and its table of feedback terms. The
* polynomial is
*  $X^{32}+X^{26}+X^{23}+X^{22}+X^{16}+X^{12}+X^{11}+X^{10}+X^8+X^7+X^5+X^4+X^2+X^1+X^0$ 
*
* Note that we take it "backwards" and put the highest-order term in
* the lowest-order bit. The  $X^{32}$  term is "implied"; the LSB is the
*  $X^{31}$  term, etc. The  $X^0$  term (usually shown as "+1") results in
* the MSB being 1
*
* Note that the usual hardware shift register implementation, which
* is what we're using (we're merely optimizing it by doing eight-bit
* chunks at a time) shifts bits into the lowest-order term. In our
* implementation, that means shifting towards the right. Why do we
* do it this way? Because the calculated CRC must be transmitted in
* order from highest-order term to lowest-order term. UARTs transmit
* characters in order from LSB to MSB. By storing the CRC this way
* we hand it to the UART in the order low-byte to high-byte; the UART
* sends each low-bit to high-bit; and the result is transmission bit
* by bit from highest- to lowest-order term without requiring any bit
* shuffling on our part. Reception works similarly
*
* The feedback terms table consists of 256, 32-bit entries. Notes
*
* The table can be generated at runtime if desired; code to do so
* is shown later. It might not be obvious, but the feedback
* terms simply represent the results of eight shift/xor opera
* tions for all combinations of data and CRC register values
*
* The values must be right-shifted by eight bits by the "updcrc
* logic; the shift must be unsigned (bring in zeroes). On some
* hardware you could probably optimize the shift in assembler by
* using byte-swap instructions
* polynomial $edb88320
*
*
* CRC32 code derived from work by Gary S. Brown.
*/
p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/nan/wlan_nan_crc.c

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// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
// $Id: //components/rel/wlanfw_cmn.cnss/1.0/cnss_proc/wlan/fwcommon/include/queue.h#1 $
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p4://qctp411.qualcomm.com/components/rel/wlanfw_cmn.cnss/1.0.0/include/queue.h

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/wlan/proto/include/if_llc.h#3 $
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 *   @(#)if_llc.h  8.1 (Berkeley) 6/10/93
 * $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
 */

p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/if_llc.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/patch/AR6320/v3/shal_patch.c

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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/mywpa_common.c

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p4://qctp411.qualcomm.com/components/rel/wlanfw\_protocol\_cmnnss/1.0.0/src/supplicant/mywpa\_common.h

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p4://qctp411.qualcomm.com/components/rel/wlanfw\_mac\_core.cnss/1.0/src/os/common/cmnnos\_printf.c

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 */
/*original files: ccx_cckm.c and part of wpa_common.c in proprietary patch
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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/src/supplicant/mywpa_cckm.c

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/wlan/proto/include/if_llc.h#3 $
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* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
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p4://qctp411.qualcomm.com/components/rel/wlanfw_protocol_cmn.cnss/1.0.0/include/if_llc.h

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* AES (Rijndael) cipher
*
* Modifications to public domain implementation:
* - support only 128-bit keys
* - cleanup
* - use C pre-processor to make it easier to change S table access
* - added option (AES_SMALL_TABLES) for reducing code size by about 8 kB at
*   cost of reduced throughput (quite small difference on Pentium 4,
*   10-25% when using -O1 or -O2 optimization)
*
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/*
* rijndael-alg-fst.c
*
* @version 3.0 (December 2000)
*
* Optimised ANSI C code for the Rijndael cipher (now AES)

```

```

*
* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>
* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>
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* AES-based functions
*
* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
* - One-Key CBC MAC (OMAC1) hash with AES-128
* - AES-128 CTR mode encryption
* - AES-128 EAX mode encryption/decryption
* - AES-128 CBC
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//
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// The algorithm is due to Ron Rivest. This code was
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//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
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//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/misc/crypto/md5.c

/*=====
@file secrsa_encryption.c

This file provides RSA PKCS #1 v1.5 encryption/decryption implementation.
=====

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$Header:
//components/dev/wlanfw.cnss/1.0.1/dennis.rome_emu_es2_rel_fw/cnss_proc/wlan/fw/target/src/misc/binsig/rsa.c#2 $
$DateTime: 2013/10/21 08:09:59 $
$Author: changhon $

when    who    what, where, why
-----  --  -----
04/11/11  qxu    initial version

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=====*/
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/misc/binsig/rsa.c

/*=====

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* $File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/include/cil.h $
* $Revision: #3 $
* $Date: 2013/08/06 $
* $Change: 2161544 $
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* $File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/include/dev.h $
* $Revision: #3 $
* $Date: 2013/08/06 $
* $Change: 2161544 $
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/\*=====

\* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss\_proc/wlan/fw/target/include/pcd.h \$  
\* \$Revision: #3 \$  
\* \$Date: 2013/08/06 \$  
\* \$Change: 2161544 \$  
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/\*=====

\* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss\_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/cil.c \$  
\* \$Revision: #3 \$  
\* \$Date: 2013/08/06 \$  
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/\*\* @file

\*  
 \* The Core Interface Layer provides basic services for accessing and  
 \* managing the DWC\_usb3 hardware. These services are used by both the  
 \* Peripheral Controller Driver and the On The Go Driver.  
 \*  
 \* The CIL manages the memory map for the core so that the PCD and OTG drivers  
 \* don't have to do this separately. The CIL also performs basic services  
 \* that are not specific to either the Device or OTG modes of operation.  
 \* These services include all functionality that requires specific  
 \* knowledge of the CSR layout or the DMA descriptor (TRB) layout. Also  
 \* included are services for invoking each of the commands provided by  
 \* the DGCMD and DEPCMD registers (see the "Control and Status Registers"  
 \* chapter of the USB3 controller databook for details).  
 \*  
 \* The Core Interface Layer has the following requirements:  
 \* - Provides basic controller operations.  
 \* - Minimal use of OS services.  
 \* - The OS services used will be abstracted by using inline functions  
 \* or macros.

\*/

p4://qctp411.qualcomm.com/components/rel/wlanfw\_mac\_core.cnss/1.0/src/soc/usbhwcore/drv/snps/cil.c

/\* =====  
 \* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss\_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/pcd\_intr.c \$  
 \* \$Revision: #3 \$  
 \* \$Date: 2013/08/06 \$  
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/\*\* @file

\* This file contains the implementation of the PCD Interrupt handlers.

\*

\* The PCD handles the device interrupts. Many conditions can cause a  
\* device interrupt. When an interrupt occurs, the device interrupt  
\* service routine determines the cause of the interrupt and  
\* dispatches handling to the appropriate routine. These interrupt  
\* handling routines are described below.

\*/

p4://qctp411.qualcomm.com/components/rel/wlanfw\_mac\_core.cnss/1.0/src/soc/usbhwcore/drv/snps/pcd\_intr.c

/\* =====

\* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss\_proc/wlan/fw/target/include/os\_dev.h \$

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/\* =====

\* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss\_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/cil\_intr.c \$

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*===== */

/** @file
*
* The Core Interface Layer provides basic services for accessing and
* managing the DWC_usb3 hardware. These services are used by both the
* Peripheral Controller Driver and the On The Go Driver.
*
* This file contains the common interrupt handling functions.
*/
p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/soc/usbhwcore/drv/snps/cil_intr.c

/*=====
* $File: //components/rel/wlanfw.cnss/1.0.1/cnss_proc/wlan/fw/target/src/soc/usbhwcore/drv/snps/pcd.c $
* $Revision: #3 $
* $Date: 2013/08/06 $
* $Change: 2161544 $
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/\*\* @file

\* This file implements the Peripheral Controller Driver.

\*

\* The Peripheral Controller Driver (PCD) is responsible for translating  
\* requests from the Function Driver into the appropriate actions on the  
\* DWC\_usb3 controller. It isolates the Function Driver from the specifics  
\* of the controller by providing an API to the Function Driver.

\*

\* The Peripheral Controller Driver for Linux will implement the Gadget API,  
\* so that the existing Gadget drivers can be used. (Gadget Driver is the  
\* Linux terminology for a Function Driver.)

\*

\* The Linux Gadget API is defined in the header file  
\* `<linux/usb/gadget.h>`. The USB EP operations API is defined  
\* in the structure `usb_ep_ops` and the USB Controller API is  
\* defined in the structure `usb_gadget_ops`.

\*

\* An important function of the PCD is managing interrupts generated by the  
\* DWC\_usb3 controller. The implementation of the DWC\_usb3 device mode  
\* interrupt service routines is in `pcd_intr.c`.

\*/

p4://qctp411.qualcomm.com/components/rel/wlanfw\_mac\_core.cnss/1.0/src/soc/usbhwcore/drv/snps/pcd.c

/\* =====

\* \$File: //components/rel/wlanfw.cnss/1.0.1/cnss\_proc/wlan/fw/target/include/usb3\_hw.h \$

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//
// Name:      lzuncompr.c
// Author:    Vitaly Luban
// Based on code from Marcus Geelnard
// Description: LZ77 decoder implementation.
// Reentrant: Yes
//
// -----
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// marcus.geelnard at home.se
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p4://qctp411.qualcomm.com/components/rel/wlanfw_mac_core.cnss/1.0/src/misc/lzuncompr/lzuncompr1.c

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//  
// @(#)queue.h 8.5 (Berkeley) 8/20/94  
// \$FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp \$  
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```

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 *
 * - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
 * - One-Key CBC MAC (OMAC1) hash with AES-128
 * - AES-128 CTR mode encryption
 * - AES-128 EAX mode encryption/decryption
 * - AES-128 CBC
 *
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 *

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\* AES (Rijndael) cipher

\*

\* Modifications to public domain implementation:

\* - support only 128-bit keys

\* - cleanup

\* - use C pre-processor to make it easier to change S table access

\* - added option (AES\_SMALL\_TABLES) for reducing code size by about 8 kB at

\* cost of reduced throughput (quite small difference on Pentium 4,

\* 10-25% when using -O1 or -O2 optimization)

\*

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\* rijndael-alg-fst.c

\*

\* @version 3.0 (December 2000)

\*

\* Optimised ANSI C code for the Rijndael cipher (now AES)

\*

\* @author Vincent Rijmen <vincent.rijmen@esat.kuleuven.ac.be>

\* @author Antoon Bosselaers <antoon.bosselaers@esat.kuleuven.ac.be>

\* @author Paulo Barreto <paulo.barreto@terra.com.br>

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\* AES-based functions  
\*  
\* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)  
\* - One-Key CBC MAC (OMAC1) hash with AES-128  
\* - AES-128 CTR mode encryption  
\* - AES-128 EAX mode encryption/decryption  
\* - AES-128 CBC  
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* This file defines the cryptographic functions that need to be implemented
* for wpa_supplicant and hostapd. When TLS is not used, internal
* implementation of MD5, SHA1, and AES is used and no external libraries are
* required. When TLS is enabled (e.g., by enabling EAP-TLS or EAP-PEAP), the
* crypto library used by the TLS implementation is expected to be used for
* non-TLS needs, too, in order to save space by not implementing these
* functions twice.
*
* Wrapper code for using each crypto library is in its own file (crypto*.c)
* and one of these files is build and linked in to provide the functions
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*/

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//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
// except that you don't need to include two pages of legalese
// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//

```



```

/*=====
@file secrsa_encryption.c

This file provides RSA PKCS #1 v1.5 encryption/decryption implementation.
=====

                        EDIT HISTORY FOR FILE
$DateTime: 2013/08/06 21:06:51 $
$Author: pdhaval $

when    who    what, where, why
-----  --  -----
04/11/11  qxu    initial version

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//
// Name:    lzuncompr.c
// Author:   Vitaly Luban
// Based on code from Marcus Geelnard
// Description: LZ77 decoder implementation.
// Reentrant: Yes
//
// -----
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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
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*
* @(#)if_llc.h 8.1 (Berkeley) 6/10/93
* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
*/

```

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src/wlan/proto/include/if\_llc.h#1

```

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src/wlan/proto/include/if\_ethersubr.h#1

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//
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/misc/crypto/md5.c#1 $
//
// This code implements the MD5 message-digest algorithm.
// The algorithm is due to Ron Rivest. This code was
// written by Colin Plumb in 1993, no copyright is claimed.
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//
// Equivalent code is available from RSA Data Security, Inc.
// This code has been tested against that, and is equivalent,
// except that you don't need to include two pages of legalese
// with every copy.
//
// To compute the message digest of a chunk of bytes, declare an
// MD5Context structure, pass it to MD5Init, call MD5Update as
// needed on buffers full of bytes, and then call MD5Final, which
// will fill a supplied 16-byte array with the digest.
//
```

src/misc/crypto/md5.c#1

```
=====
=====
```

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```
support/lz77/lz.h#1
support/lz77/lz.c#1
```

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```
src/misc/lzuncompr/lzuncompr1.c#1
tests/serflash/flashprog/uncompr.c#1
```

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//
// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//

src/wlan/include/queue.h#1
../include/queue.h#1
```

---

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=====
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```

```

/*
 * AES-based functions
 *
 * - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
 * - One-Key CBC MAC (OMAC1) hash with AES-128
 * - AES-128 CTR mode encryption
 * - AES-128 EAX mode encryption/decryption
 * - AES-128 CBC
 *
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/\*  
\* \$DateTime: 2013/07/24 11:35:54 \$  
\*/

/\* \$OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp \$ \*/  
/\* \$NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp \$ \*/

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 * $DateTime: 2011/01/07 17:50:19 $
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/* $OpenBSD: strcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp $ */
```

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 $
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*
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* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
*/

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src/wlan/proto/include/if_llc.h#1
```

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```
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```

```
//
```

```
// This code implements the MD5 message-digest algorithm.  
// The algorithm is due to Ron Rivest. This code was  
// written by Colin Plumb in 1993, no copyright is claimed.  
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```

```
//
```

```
// Equivalent code is available from RSA Data Security, Inc.  
// This code has been tested against that, and is equivalent,  
// except that you don't need to include two pages of legalese  
// with every copy.
```

```
//
```

```
// To compute the message digest of a chunk of bytes, declare an  
// MD5Context structure, pass it to MD5Init, call MD5Update as  
// needed on buffers full of bytes, and then call MD5Final, which  
// will fill a supplied 16-byte array with the digest.
```

```
//
```

```
src/misc/crypto/md5.c#1
```

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support/lz77/lz.h#1
support/lz77/lz.c#1
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```
src/misc/lzuncompr/lzuncompr1.c#1  
tests/serflash/flashprog/uncompr.c#1
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// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//
```

```
src/wlan/include/queue.h#1
../include/queue.h#1
```

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// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 $
// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 $
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*
* @(#)if_llc.h 8.1 (Berkeley) 6/10/93
* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
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src/wlan/proto/include/if\_llc.h#1

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src/wlan/proto/include/if\_ethersubr.h#1

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// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/misc/crypto/md5.c#1 $  
//  
// This code implements the MD5 message-digest algorithm.  
// The algorithm is due to Ron Rivest. This code was  
// written by Colin Plumb in 1993, no copyright is claimed.  
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//  
// Equivalent code is available from RSA Data Security, Inc.  
// This code has been tested against that, and is equivalent,  
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//  
// To compute the message digest of a chunk of bytes, declare an  
// MD5Context structure, pass it to MD5Init, call MD5Update as  
// needed on buffers full of bytes, and then call MD5Final, which  
// will fill a supplied 16-byte array with the digest.  
//  
  
src/misc/crypto/md5.c#1
```

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```
support/lz77/lz.h#1
support/lz77/lz.c#1
```

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```
src/misc/lzuncompr/lzuncompr1.c#1
tests/serflash/flashprog/uncompr.c#1
```

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// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//
```

```
src/wlan/include/queue.h#1
../include/queue.h#1
```

```
=====
=====

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/\*  
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*
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* - AES-128 CTR mode encryption
* - AES-128 EAX mode encryption/decryption
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```

```

/*
* $DateTime: 2013/07/24 11:35:54 $
*/

/* $OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp $ */
/* $NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp $ */

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\* \$DateTime: 2011/01/07 17:50:19 \$  
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/\* \$OpenBSD: strcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp \$ \*/

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// $NetBSD: if_llc.h,v 1.12 1999/11/19 20:41:19 thorpej Exp $
// $Id: //depot/sw/qca_main/components/wlan/qca-wifi-fw/1.0/drivers/target/src/wlan/proto/include/if_llc.h#2 $
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*
* @(#)if_llc.h 8.1 (Berkeley) 6/10/93
* $FreeBSD: src/sys/net/if_llc.h,v 1.9 2002/09/23 06:25:08 alfred Exp $
*/

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wlan/protocol/src/include/if\_llc.h#1

```

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wlan/protocol/include/if\_ETHERSUBR.h#1

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wlan/protocol/support/lz77/lz.h#1  
wlan/protocol/support/lz77/lz.c#1

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```
wlan/mac_core/tests/serflash/flashprog/uncompr.c#1
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// @(#)queue.h 8.5 (Berkeley) 8/20/94
// $FreeBSD: src/sys/sys/queue.h,v 1.58 2004/04/07 04:19:49 imp Exp $
//
```

wlan/include/queue.h#1

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 * SHA1 hash implementation and interface functions
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```
wlan/mac_core/include/sha1.h
wlan/mac_core/romexport/AR900B/hw.1/include/sha1.h
wlan/mac_core/romexport/AR900B/hw.2/include/sha1.h
```

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```
=====
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 * FILE:    sha2.h
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* $Id: //depot/sw/branches/qca_rome_main/perf_pwr_offload/drivers/target/include/sha2.h#1 $
*/

```

```

wlan/mac_core/romexport/AR900B/hw.1/include/sha2.h
wlan/mac_core/romexport/AR900B/hw.2/include/sha2.h

```

```

/*
* AES-based functions
*
*
* - AES Key Wrap Algorithm (128-bit KEK) (RFC3394)
* - One-Key CBC MAC (OMAC1) hash with AES-128
* - AES-128 CTR mode encryption
* - AES-128 EAX mode encryption/decryption
* - AES-128 CBC
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```

wlan/mac_core/include/aes_wrap.h
wlan/mac_core/romexport/AR900B/hw.1/include/aes_wrap.h
wlan/mac_core/romexport/AR900B/hw.2/include/aes_wrap.h

```

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wlan/mac\_core/romexport/AR900B/hw.1/include/md5.h  
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wlan/mac\_core/romexport/AR900B/hw.1/include/seccommon.h  
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wlan/mac\_core/include/usb.h  
 wlan/mac\_core/romexport/AR900B/hw.1/include/usb.h  
 wlan/mac\_core/romexport/AR900B/hw.2/include/usb.h

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* ===== */
```

wlan/mac\_core/include/cil.h  
 wlan/mac\_core/romexport/AR900B/hw.1/include/cil.h  
 wlan/mac\_core/romexport/AR900B/hw.2/include/cil.h  
 wlan/mac\_core/include/dev.h  
 wlan/mac\_core/romexport/AR900B/hw.1/include/dev.h

```
wlan/mac_core/romexport/AR900B/hw.2/include/dev.h
wlan/mac_core/include/os_dev.h
wlan/mac_core/romexport/AR900B/hw.1/include/os_dev.h
wlan/mac_core/romexport/AR900B/hw.2/include/os_dev.h
wlan/mac_core/include/usb3_hw.h
wlan/mac_core/romexport/AR900B/hw.1/include/usb3_hw.h
wlan/mac_core/romexport/AR900B/hw.2/include/usb3_hw.h
wlan/mac_core/include/pcd.h
wlan/mac_core/romexport/AR900B/hw.1/include/pcd.h
wlan/mac_core/romexport/AR900B/hw.2/include/pcd.h
```

```
/*
 * $Header: //source/qcom/qct/core/api/kernel/main/latest/libstd/stringl/stringl.h#13 $
 * $DateTime: 2013/07/24 11:35:54 $
 */
```

```
/* $OpenBSD: string.h,v 1.17 2006/01/06 18:53:04 millert Exp $ */
/* $NetBSD: string.h,v 1.6 1994/10/26 00:56:30 cgd Exp $ */
```

```
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 *
 * @(#)string.h 5.10 (Berkeley) 3/9/91
 */
```

```
wlan/protocol/support/bin2srec/include/stringl/stringl.h
```

```
/*
 * $Header: //source/qcom/qct/core/kernel/libstd/main/latest/src/strncpy.c#1 $
```

\* \$DateTime: 2011/01/07 17:50:19 \$  
\*/

/\* \$OpenBSD: strlcpy.c,v 1.11 2006/05/05 15:27:38 millert Exp \$ \*/

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\*/

wlan/protocol/support/bin2src/strlcpy.c

=====  
=====  
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\*/

wlan/mac\_core/patch/AR900B/v2/cmnos\_printf\_patch.c

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By Tim Baverstock <warwick@mmm.co.uk>, Multi Media Machine Ltd.  
5 March 1997

Stuff stolen from pam\_rootok and pam\_listfile

Changes by Tomas Mraz <tmraz@redhat.com> 5 January 2005, 26 January 2006  
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The source for the has is derived CommonCrypto files CommonDigest.h, md32\_common.h, md5\_locl.h, md5\_locl.h, and  
openssl/md5.h.

The following changes have been made to the original sources:

- replaced CC\_LONG w/ mDNSu32
- replaced CC\_MD5\* with MD5\*
- replaced CC\_LONG w/ mDNSu32, removed conditional #defines from md5.h
- removed extern decls for MD5\_Init/Update/Final from CommonDigest.h
- removed APPLE\_COMMON\_DIGEST specific #defines from md5\_locl.h

Note: machine archetecure specific conditionals from the original sources are turned off, but are left in the code  
to aid in platform-specific optimizations and debugging.

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md5\_dgst.c, md5\_locl.h

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- Ben Skeggs (darktama, darktama\_)

- B. R. <koala\_br@users.sourceforge.net> (koala\_br)

- Carlos Martin <carlosmn@users.sf.net> (carlosmn)

- Christoph Bumiller <e0425955@student.tuwien.ac.at> (calim, chrisbmr)

- Dawid Gajownik <gajownik@users.sf.net> (gajownik)

- Dmitry Baryshkov

- Dmitry Eremin-Solenikov <lumag@users.sf.net> (lumag)

- EdB <edb\_@users.sf.net> (edb\_)

- Erik Waling <erikwaling@users.sf.net> (erikwaling)

- Francisco Jerez <currojerez@riseup.net> (curro)

- imirkin <imirkin@users.sf.net> (imirkin)

- jb17bsome <jb17bsome@bellsouth.net> (jb17bsome)

- Jeremy Kolb <kjeremy@users.sf.net> (kjeremy)

- Laurent Carlier <lordheavym@gmail.com> (lordheavy)

- Luca Barbieri <luca@luca-barbieri.com> (lb, lb1)

- Maarten Maathuis <madman2003@gmail.com> (stillunknown)
- Marcin Kościelnicki <koriakin@0x04.net> (mwk, koriakin)
- Mark Carey <mark.carey@gmail.com> (careym)
- Matthieu Castet <matthieu.castet@parrot.com> (mat-c)
- nvidiaman <nvidiaman@users.sf.net> (nvidiaman)
- Patrice Mandin <patmandin@gmail.com> (pmandin, pmdata)
- Pekka Paalanen <pq@iki.fi> (pq, ppaalanen)
- Peter Popov <ironpeter@users.sf.net> (ironpeter)
- Richard Hughes <hughsient@users.sf.net> (hughsient)
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- Victor Stinner <victor.stinner@haypocalc.com> (haypo)
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Jean-loup Gailly      Mark Adler  
gzip@prep.ai.mit.edu    madler@alumni.caltech.edu

-----  
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The following individuals contributed in part to the Network Time Protocol Distribution Version 4 and are acknowledged as authors of this work.

1. [1]Takao Abe <takao\_abe@xurb.jp> Clock driver for JJY receivers
2. [2]Mark Andrews <mark\_andrews@isc.org> Leitch atomic clock controller
3. [3]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices
4. [4]Viraj Bais <vbais@mailman1.intel.com> and [5]Clayton Kirkwood <kirkwood@striderfin.intel.com> port to WindowsNT 3.5
5. [6]Michael Barone <michael,barone@lmco.com> GPSVME fixes
6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option
7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.
8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver
9. [10]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support
10. [11]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in snpt
11. [12]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support

12. [13]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)
13. [14]Steve Clift <clift@ml.csiro.au> OMEGA clock driver
14. [15]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration
15. [16]Sven Dietrich <sven\_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.
16. [17]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port
17. [18]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port
18. [19]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119
19. [20]John Hay <jhay@icomtek.csiro.co.za> IPv6 support and testing
20. [21]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite
21. [22]Claas Hilbrecht <neoclock4x@linum.com> NeoClock4X clock driver
22. [23]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver
23. [24]Mike Iglesias <iglesias@uci.edu> DEC Alpha port
24. [25]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port
25. [26]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul
26. [27]Hans Lambermont <Hans.Lambermont@nl.origin-it.com> or [28]<H.Lambermont@chello.nl> ntpswEEP
27. [29]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)
28. [30]Frank Kardel [31]<kardel (at) ntp (dot) org> PARSE <GENERIC> driver (>14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling
29. [32]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HP-UX modifications
30. [33]Dave Katz <dkatz@cisco.com> RS/6000 AIX port
31. [34]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver
32. [35]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port
33. [36]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication
34. [37]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305
35. [38]Danny Mayer <mayer@ntp.org> Network I/O, Windows Port, Code Maintenance
36. [39]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG
37. [40]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port
38. [41]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility
39. [42]Tom Moore <tmoore@fivel.daytonoh.ncr.com> i386 svr4 port
40. [43]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port
41. [44]Derek Mulcahy <derek@toybox.demon.co.uk> and [45]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver
42. [46]Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code maintenance
43. [47]Rainer Pruy <Rainer.Pruy@informatik.uni-erlangen.de> monitoring/trap scripts, statistics file handling
44. [48]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port
45. [49]Wilfredo S 禡 chez <wsanchez@apple.com> added support for NetInfo
46. [50]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules
47. [51]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory
48. [52]Ray Schnitzler <schnitz@unipress.com> Unixware1 port
49. [53]Michael Shields <shields@tembel.org> USNO clock driver

50. [54]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver
  51. [55]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)
  52. [56]Kenneth Stone <ken@sdd.hp.com> HP-UX port
  53. [57]Ajit Thyagarajan <ajit@ee.udel.edu> IP multicast/anycast support
  54. [58]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp> TRAK clock driver
  55. [59]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver
  56. [60]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD
- 

## References

1. mailto:%20takao\_abe@xurb.jp
2. mailto:%20mark\_andrews@isc.org
3. mailto:%20altmeier@atsoft.de
4. mailto:%20vbais@mailman1.intel.co
5. mailto:%20kirkwood@striderfm.intel.com
6. mailto:%20michael.barone@lmco.com
7. mailto:%20karl@owl.HQ.ileaf.com
8. mailto:%20greg.brackley@bigfoot.com
9. mailto:%20Marc.Brett@westgeo.com
10. mailto:%20Piete.Brooks@cl.cam.ac.uk
11. mailto:%20nelson@bolyard.me
12. mailto:%20Jean-Francois.Boudreault@viagenie.qc.ca
13. mailto:%20reg@dwf.com
14. mailto:%20clift@ml.csiro.au
15. mailto:casey@csc.co.za
16. mailto:%20Sven\_Dietrich@trimble.COM
17. mailto:%20dundas@salt.jpl.nasa.gov
18. mailto:%20duwe@immd4.informatik.uni-erlangen.de
19. mailto:%20dennis@mrbill.canet.ca
20. mailto:%20jhay@icomtek.csir.co.za
21. mailto:%20davehart@davehart.com
22. mailto:%20neoclock4x@linum.com
23. mailto:%20glenn@herald.usask.ca
24. mailto:%20iglesias@uci.edu
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26. mailto:%20jbj@chatham.usdesign.com
27. mailto:Hans.Lambermont@nl.origin-it.com
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31. mailto:%20kardel(at)ntp(dot)org
32. mailto:%20jones@hermes.chpc.utexas.edu
33. mailto:%20dkatz@cisco.com
34. mailto:%20leres@ee.lbl.gov
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36. mailto:%20louie@ni.umd.edu
37. mailto:%20thorinn@diku.dk
38. mailto:%20mayer@ntp.org
39. mailto:%20mills@udel.edu
40. mailto:%20moeller@gwdgv1.dnet.gwdg.de
41. mailto:%20mogul@pa.dec.com
42. mailto:%20tmoore@fivel.daytonoh.ncr.com
43. mailto:%20kamal@whence.com

44. mailto:%20derek@toybox.demon.co.uk  
45. mailto:%20d@hd.org  
46. mailto:%20neal@ntp.org  
47. mailto:%20Rainer.Pruy@informatik.uni-erlangen.de  
48. mailto:%20dirce@zk3.dec.com  
49. mailto:%20wsanchez@apple.com  
50. mailto:%20mrapple@quack.kfu.com  
51. mailto:%20jack@innovativeinternet.com  
52. mailto:%20schnitz@unipress.com  
53. mailto:%20shields@tembel.org  
54. mailto:%20pebbles.jpl.nasa.gov  
55. mailto:%20harlan@pfcs.com  
56. mailto:%20ken@sdd.hp.com  
57. mailto:%20ajit@ee.udel.edu  
58. mailto:%20tsuruoka@nc.fukuoka-u.ac.jp  
59. mailto:%20vixie@vix.com  
60. mailto:%20Ulrich.Windl@rz.uni-regensburg.de

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G. Lowe, US Patent 6,711,293 (March 23, 2004). Provisional application  
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2009 Xr <[xr@i-jeuxvideo.com](mailto:xr@i-jeuxvideo.com)>

2007 Justin Pryzby <[justinpryzby@users.sourceforge.net](mailto:justinpryzby@users.sourceforge.net)>



2004 Reini Urban <rurban@x-ray.at>

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- a. If it is being maintained, then ask the Current Maintainer to update their communication data within one month.

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Requests includes some vendorized python libraries to ease installation.

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PCRE is a library of functions to support regular expressions whose syntax and semantics are as close as possible to those of the Perl 5 language.

Written by: Philip Hazel <ph10@cam.ac.uk>

University of Cambridge Computing Service,  
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Written in 2012 by  
Jean-Philippe Aumasson <jeanphilippe.aumasson@gmail.com>

Daniel J. Bernstein <djb@cr.yp.to>

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Jean-loup Gailly  
jloup@gzip.org

Mark Adler  
madler@alumni.caltech.edu

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An excellent reference on the underlying algorithm (and related algorithms) is:

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Code inspired from libuatomic\_ops-1.2, inherited in part from the Boehm-Demers-Weiser conservative garbage collector.

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## [Exhibit-280]

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This package was put together by Wichert Akkerman <wakkerma@debian.org> from sources obtained from: <ftp://ftp.vim.org/pub/vim/unix/>. The current maintainer is Debian VIM Maintainers <pkg-vim-maintainers@lists.alioth.debian.org>.

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Beware that some of this code is subtly aware of the way operator precedence is structured in regular expressions. Serious changes in regular-expression syntax might require a total rethink.

Changes have been made by Tony Andrews, Olaf 'Rhialto' Seibert, Robert Webb, Ciaran McCreesh and Bram Moolenaar.  
Named character class support added by Walter Briscoe (1998 Jul 01)

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Adrian Kennard, Andrews & Arnold Ltd  
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This is D3DES (V5.09) by Richard Outerbridge with the double and triple-length support removed for use in VNC. Also the bytebit[] array has been reversed so that the most significant bit in each byte of the key is ignored, not the least significant.

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Compute the Adler-32 checksum of a data stream.  
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Acoustic Echo Cancellation NLMS-pw algorithm

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This file implements some helper functions for the array assignment routines. The actual assignment routines are in array\_assign\_\*.c

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Provide multidimensional arrays as a basic object type in python.

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by

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Brigham Young University

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Numarray design (which provided guidance) by  
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---

This file contains low-level loops for data type transfers. In particular the function `PyArray_GetDTypeTransferFunction` is implemented here.

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Python Multiarray Module -- A useful collection of functions for creating and using ndarrays

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Modified for numpy in 2005

Travis E. Oliphant  
[oliphant@ee.byu.edu](mailto:oliphant@ee.byu.edu)  
Brigham Young University

---

This file implements most of the main API functions of NumPy's `nditer`. This excludes functions specialized using the templating system.

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Travis E. Oliphant 2005, 2006 [oliphant@ee.byu.edu](mailto:oliphant@ee.byu.edu) ([oliphant.travis@ieee.org](mailto:oliphant.travis@ieee.org))  
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based on the

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with inspiration and code from  
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Authors:  
Pearu Peterson <pearu@cens.ioc.ee>, February 2002  
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In 2006, the package was completely rewritten by Pierre Gerard-Marchant  
(University of Georgia) to make the MaskedArray class a subclass of ndarray,  
and to improve support of structured arrays.

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Subclassing of the base 'ndarray' 2006 by Pierre Gerard-Marchant  
(pgmdevlist\_AT\_gmail\_DOT\_com)  
Improvements suggested by Reggie Dugard (reggie\_AT\_merfinllc\_DOT\_com)

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<https://gist.github.com/inneme/f1f7821f07cf76504a97f6537e818083>

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